

74F86 2-Input Exclusive-OR Gate

General Description

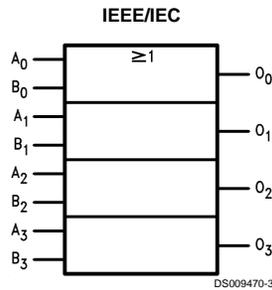
This device contains four independent gates, each of which performs the logic exclusive-OR function.

Ordering Code:

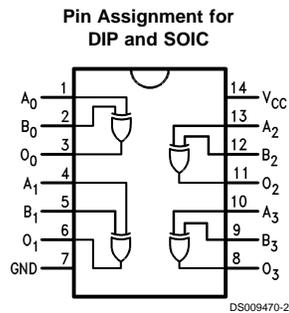
Commercial	Package Number	Package Description
74F86PC	N14A	14-Lead (0.300" Wide) Molded Dual-in-Line
74F86SC (Note 1)	M14A	14-Lead (0.150" Wide) Molded Small Outline, JEDEC
74F86SJ (Note 1)	M14D	14-Lead (0.300" Wide) Molded Small Outline, EIAJ

Note 1: Devices also available in 13" reel. Use suffix = SCX and SJX.

Logic Symbol



Connection Diagram



Unit Loading/Fan Out

Pin Names	Description	U.L. HIGH/LOW	Input I_{IH}/I_{IL} Output I_{OH}/I_{OL}
A_n, B_n	Inputs	1.0/1.0	20 μ A/-0.6 mA
O_n	Outputs	50/33.3	-1 mA/20 mA

Absolute Maximum Ratings (Note 2)

Storage Temperature	-65°C to +150°C
Ambient Temperature under Bias	-55°C to +125°C
Junction Temperature under Bias	-55°C to +175°C
Plastic	-55°C to +150°C
V _{CC} Pin Potential to Ground Pin	-0.5V to +7.0V
Input Voltage (Note 3)	-0.5V to +7.0V
Input Current (Note 3)	-30 mA to +5.0 mA
Voltage Applied to Output in HIGH State (with V _{CC} = 0V)	
Standard Output	-0.5V to V _{CC}
3-STATE Output	-0.5V to +5.5V

Current Applied to Output in LOW State (Max) twice the rated I_{OL} (mA)

Recommended Operating Conditions

Free Air Ambient Temperature Commercial	0°C to +70°C
Supply Voltage Commercial	+4.5V to +5.5V

Note 2: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

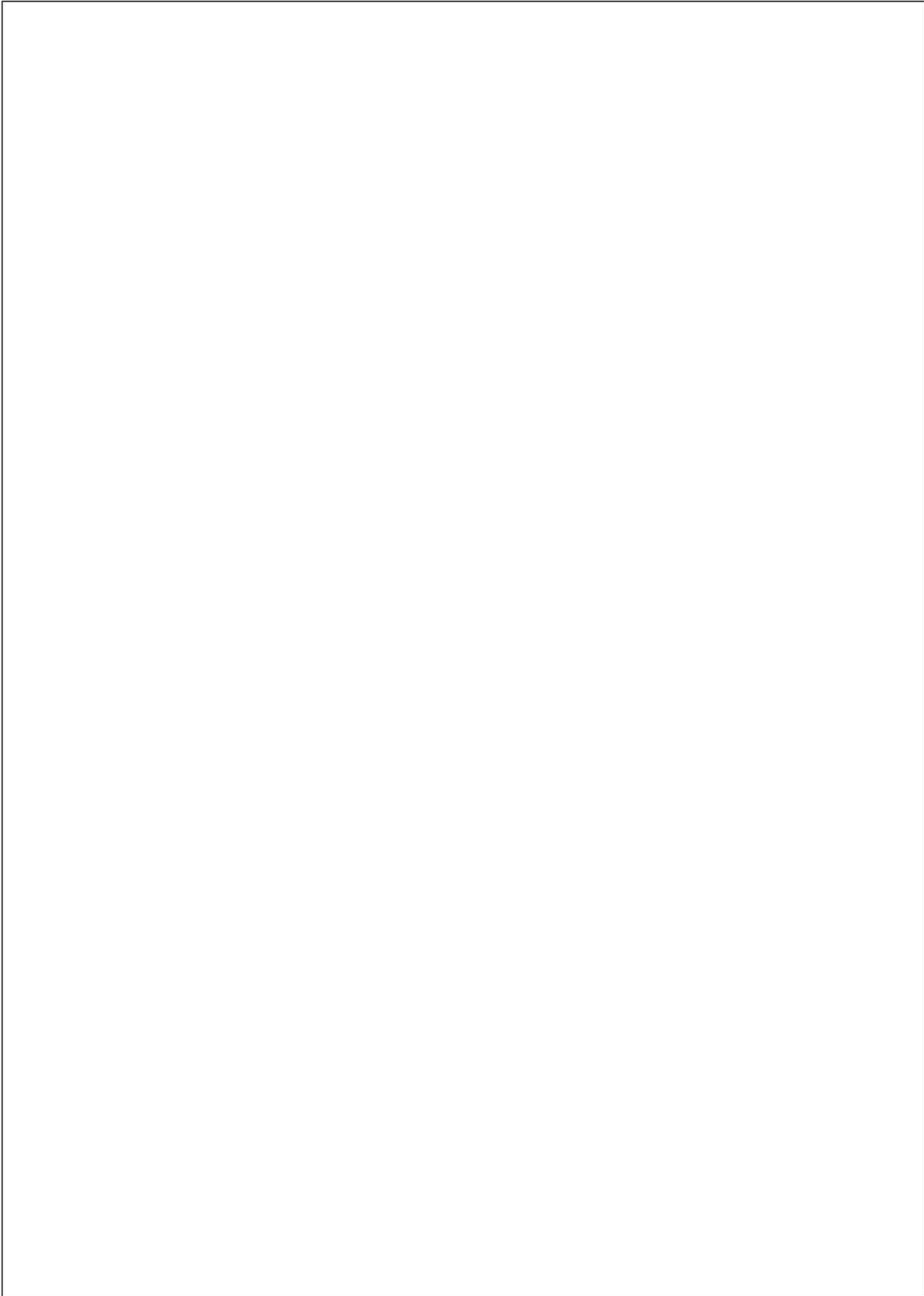
Note 3: Either voltage limit or current limit is sufficient to protect inputs.

DC Electrical Characteristics

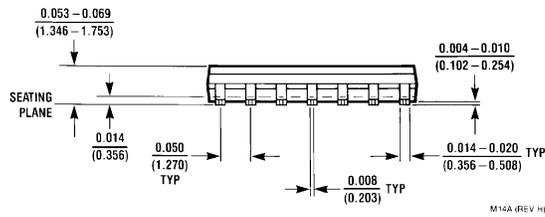
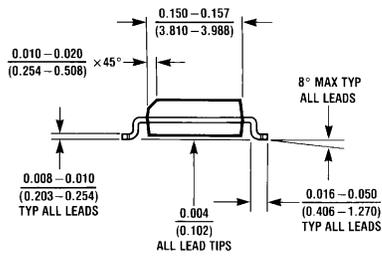
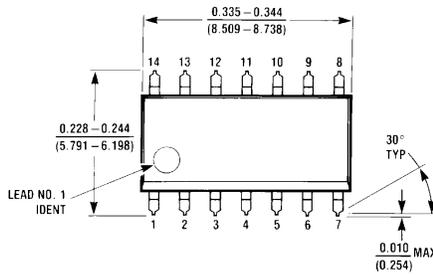
Symbol	Parameter	Min	Typ	Max	Units	V _{CC}	Conditions
V _{IH}	Input HIGH Voltage	2.0			V		Recognized as a HIGH Signal
V _{IL}	Input LOW Voltage			0.8	V		Recognized as a LOW Signal
V _{CD}	Input Clamp Diode Voltage			-1.2	V	Min	I _{IN} = -18 mA
V _{OH}	Output HIGH Voltage	10% V _{CC} 5% V _{CC}	2.5 2.7		V	Min	I _{OH} = -1 mA I _{OH} = -1 mA
V _{OL}	Output LOW Voltage	10% V _{CC}		0.5		Min	I _{OL} = 20 mA
I _{IH}	Input HIGH Current			5.0	μA	Max	V _{IN} = 2.7V
I _{BVI}	Input HIGH Current Breakdown Test			7.0	μA	Max	V _{IN} = 7.0V
I _{CEX}	Output HIGH Leakage Current			50	μA	Max	V _{OUT} = V _{CC}
V _{ID}	Input Leakage Test	4.75			V	0.0	I _{ID} = 1.9 μA All other pins grounded
I _{OD}	Output Leakage Circuit Current			3.75	μA	0.0	V _{IOD} = 150 mV All other pins grounded
I _{IL}	Input LOW Current			-0.6	mA	Max	V _{IN} = 0.5V
I _{OS}	Output Short-Circuit Current	-60		-150	mA	Max	V _{OUT} = 0V
I _{CCH}	Power Supply Current		12	18	mA	Max	V _O = HIGH
I _{CCL}	Power Supply Current		18	28	mA	Max	V _O = LOW

AC Electrical Characteristics

Symbol	Parameter	T _A = +25°C V _{CC} = +5.0V C _L = 50 pF			T _A , V _{CC} = Com C _L = 50 pF		Units
		Min	Typ	Max	Min	Max	
t _{PLH}	Propagation Delay	3.0	4.0	5.5	3.0	6.5	ns
t _{PHL}	A _n , B _n to O _n (Other Input LOW)	3.0	4.2	5.5	3.0	6.5	
t _{PLH}	Propagation Delay	3.5	5.3	7.0	3.5	8.0	ns
t _{PHL}	A _n , B _n to O _n (Other Input HIGH)	3.0	4.7	6.5	3.0	7.5	

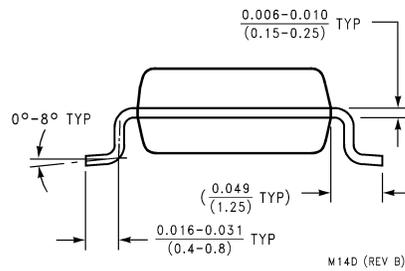
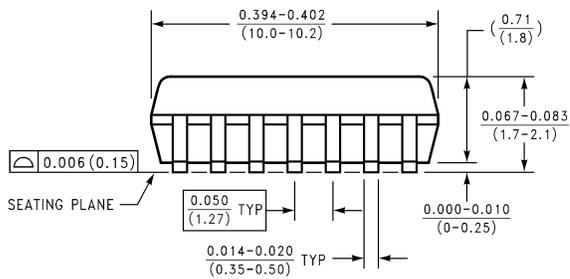
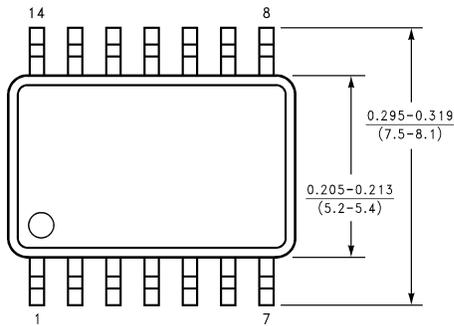


Physical Dimensions inches (millimeters) unless otherwise noted



M14A (REV H)

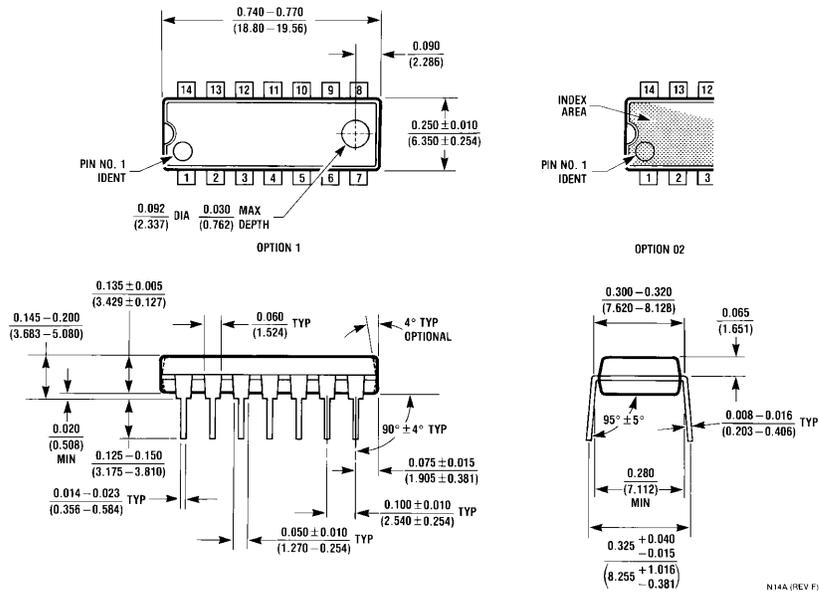
**14-Lead (0.150" Wide) Molded Small Outline Package, JEDEC (S)
Package Number M14A**



M14D (REV B)

**14-Lead (0.300" Wide) Molded Small Outline Package, EIAJ (SJ)
Package Number M14D**

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



**14-Lead (0.300" Wide) Molded Dual-In-Line Package (P)
Package Number N14A**

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