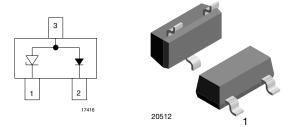


# Low Capacitance ESD Protection Diodes for High-Speed Data Interfaces



## MARKING (example only)



Bar = cathode marking YYY = type code (see table below) XX = date code

#### **FEATURES**

- IEC 61000-4-5 (lightning) see I<sub>PPM</sub> below
- ESD-protection acc. IEC 61000-4-2
   ± 8 kV contact discharge
   ± 15 kV air discharge
- SOT-23 package
- High temperature soldering guaranteed 260 °C/10 s at terminals
- Low capacitance for high speed data lines, cellular handsets, USB port protection, LAN equipment, peripherals
- AEC-Q101 qualified
- e3 Sn
- Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>





ROHS
COMPLIANT
GREEN
(5-2008)

ORDERIN	ORDERING INFORMATION								
	EN	/IRONMENTAL AN	ND QUALITY COD	E	PACKAG	ORDERING CODE (EXAMPLE)			
PART NUMBER (EXAMPLE)	AEC-Q101 LEAD (Pb)-F		MPLIANT + TERMINATIONS	TIN PLATED	3K PER 7" REEL (8 mm TAPE),			10K PER 13" REEL (8 mm TAPE),	
(L/O-Wil LL)	QUALIFIED	STANDARD	GREEN	PLAIED	15K/BOX = MOQ	10K/BOX = MOQ			
GL05T-		Е		3	-08		GL05T-E3-08		
GL05T-			G	3	-08		GL05T-G3-08		
GL05T-	Н	Е		3	-08		GL05T-HE3-08		
GL05T-	Н		G	3	-08		GL05T-HG3-08		
GL05T-		Е		3		-18	GL05T-E3-18		
GL05T-			G	3		-18	GL05T-G3-18		
GL05T-	Н	Е		3		-18	GL05T-HE3-18		
GL05T-	Н		G	3		-18	GL05T-HG3-18		

PACK	PACKAGE DATA								
DEVICE NAME	PACKAGE NAME	TYPE CODE	ENVIRONMENTAL STATUS	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS		
GL05T	SOT-23	L05	Standard	8.8 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals		
GLOST	301-23	L06	Green	8.1 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals		
GL12T	SOT-23	L12	Standard	8.8 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals		
GLIZI	001 20	L13	Green	8.1 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals		
GL15T	SOT-23	L15	Standard	8.8 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals		
GLIST	301-23	L16	Green	8.1 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals		
GL24T	SOT-23	L24	Standard	8.8 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals		
GLZ41	001-20	L25	Green	8.1 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals		



ABSOLUTE MAXIMUM RATINGS GL05T							
PARAMETER	TEST	TEST CONDITIONS		VALUE	UNIT		
Peak pulse current	8/20 μs	Pin 1-2 (pin 3 n.c.)	I <sub>PPM</sub>	25	Α		
Peak pulse power	8/20 µs waveform	Fill 1-2 (pill 3 ll.c.)	P <sub>PP</sub>	300	W		
ESD immunity	Contact discharge	Contact discharge acc. IEC 61000-4-2; 10 pulses		± 8	kV		
ESD Illillidrity	Air discharge acc.	IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 15	kV		
Blocking voltage	I <sub>B</sub> = 1 μA	Pin 2-1 or pin 2-3	$V_{B}$	70	V		
Operating temperature	Junction temperatu	ıre	TJ	- 55 to + 150	°C		
Storage temperature			T <sub>STG</sub>	- 55 to + 150	°C		

ABSOLUTE MAXIMUM RATINGS GL12T							
PARAMETER	TEST	TEST CONDITIONS		VALUE	UNIT		
Peak pulse current	8/20 µs	Pin 1-2 (pin 3 n.c.)	I <sub>PPM</sub>	12	Α		
Peak pulse power	8/20 µs waveform	Fill 1-2 (pill 3 ll.c.)	$P_PP$	300	W		
F0D ::1	Contact discharge	Contact discharge acc. IEC 61000-4-2; 10 pulses		± 8	kV		
ESD immunity	Air discharge acc. I	IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 15	kV		
Blocking voltage	I <sub>B</sub> = 1 μA	Pin 2-1 or pin 2-3	$V_{B}$	70	V		
Operating temperature	Junction temperatu	Junction temperature		- 55 to + 150	°C		
Storage temperature			T <sub>STG</sub>	- 55 to + 150	°C		

ABSOLUTE MAXIMUM RATINGS GL15T							
PARAMETER	TEST	CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	8/20 μs	Pin 1-2 (pin 3 n.c.)	I <sub>PPM</sub>	10	Α		
Peak pulse power	8/20 µs waveform	Pin 1-2 (pin 3 n.c.)	P <sub>PP</sub>	300	W		
ESD immunity	Contact discharge	acc. IEC 61000-4-2; 10 pulses		± 8	kV		
E3D IIIIIIIIIIIII	Air discharge acc. I	EC 61000-4-2; 10 pulses	$V_{ESD}$	± 15	kV		
Blocking voltage	I <sub>B</sub> = 1 μA	Pin 2-1 or pin 2-3	V <sub>B</sub>	70	V		
Operating temperature	Junction temperatu	re	T <sub>J</sub>	- 55 to + 150	°C		
Storage temperature			T <sub>STG</sub>	- 55 to + 150	°C		

ABSOLUTE MAXIMUM RATINGS GL24T							
PARAMETER	TEST	TEST CONDITIONS		VALUE	UNIT		
Peak pulse current	8/20 µs	Pin 1-2 (pin 3 n.c.)	I <sub>PPM</sub>	5	Α		
Peak pulse power	8/20 µs waveform	Fill 1-2 (pill 3 ll.c.)	P <sub>PP</sub>	300	W		
ESD immunity	Contact discharge	Contact discharge acc. IEC 61000-4-2; 10 pulses		± 8	kV		
ESD IIIIIIuriity	Air discharge acc. I	EC 61000-4-2; 10 pulses	$V_{ESD}$	± 15	kV		
Blocking voltage	I <sub>B</sub> = 1 μA	Pin 2-1 or pin 2-3	V <sub>B</sub>	70	V		
Operating temperature	Junction temperatu	re	T <sub>J</sub>	- 55 to + 150	°C		
Storage temperature			T <sub>STG</sub>	- 55 to + 150	°C		

The GLxxT contains an avalanche diode (pin 3-1) and a switching diode (pin 3-2). With pin 1 connected to the signal or data line and pin 2 connected to ground both diodes are in series (pin 3 remains unconnected). The big and robust avalanche diode, driven in reverse direction, provides the working range V<sub>RWM</sub> of 5 V, 12 V, 15 V or 24 V. Due to its size the capacitance of the avalanche diode is in the range of typ. 260 pF (GL05T) and 65 pF (GL24T). The small switching diode in series has a low capacitance of just 2.5 pF (typ.). As both diodes are in series (with pin 3 not connected) the total capacitance of both diodes measured between pin 1 and 2 is as low as the capacitance of the switching diode.

Before the GLxxT can provide this low capacitance the big capacitance of the avalanche diode has to be charged up with the first signal or data pulses. This is usually no problem for digital signals like USB or other data ports.

With the GLxxT a signal or data line can be protected against positive transients only. For negative transients another GLxxT can be used to provide a back path for the negative transients as well.

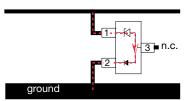


#### www.vishay.com

# Vishay Semiconductors

#### Data line

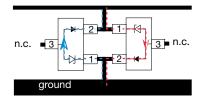
Capacitance



# Uni Unidirectional clamping performance for positive transients only.

#### Data line

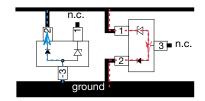
at  $V_R = 0 V$ ; f = 1 MHz



BiSy
Bidirectional and Symmetrical
clamping performance for positive
and negative transients.

#### Data line

2.5



BiAs
Bidirectional and Asymmetrical
clamping performance for positive
and negative transients.

5

pF

#### **ELECTRICAL CHARACTERISTICS GL05T** (T<sub>amb</sub> = 25 °C unless otherwise specified) pin 1 to pin 2; pin 3 not connected **PARAMETER TEST CONDITIONS/REMARKS SYMBOL** MIN. TYP. MAX. UNIT Protection paths Number of lines which can be protected $N_{\text{channel}}$ lines Reverse stand-off voltage 5 ٧ $V_{\text{RWM}}$ \_ Reverse voltage at $I_R = 20 \mu A$ 5 ٧ $V_{R}$ at $V_R = 5 \text{ V}$ 20 μΑ Reverse current $I_R$ Reverse breakdown voltage at $I_R = 1 \text{ mA}$ 6.9 7.5 8.0 ٧ $V_{BR}$ at $I_{PP} = 1 A$ \_ \_ 9.8 ٧ Reverse clamping voltage $V_{C}$ at $I_{PP} = 5 A$ 11 ٧

 $C_D$ 

<b>ELECTRICAL CHARACTERISTICS GL12T</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) pin 1 to pin 2; pin 3 not connected								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse stand-off voltage		V <sub>RWM</sub>	-	-	12	V		
Reverse voltage	at I <sub>R</sub> = 1 μA	V <sub>R</sub>	12	-	-	V		
Reverse current	at V <sub>R</sub> = 12 V	I <sub>R</sub>	-	-	1	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	13.3	14.3	17.2	V		
Payaraa alamaina valtaga	at I <sub>PP</sub> = 1 A	. V	-	-	19	V		
Reverse clamping voltage	at I <sub>PP</sub> = 5 A	V <sub>C</sub>	-	-	24	V		
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	C <sub>D</sub>	-	2.5	5	pF		

<b>ELECTRICAL CHARACTERISTICS GL15T</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) pin 1 to pin 2; pin 3 not connected									
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT			
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines			
Reverse stand-off voltage		$V_{RWM}$	-	-	15	V			
Reverse voltage	at I <sub>R</sub> = 1 μA	$V_{R}$	15	-	-	V			
Reverse current	at V <sub>R</sub> = 15 V	I <sub>R</sub>	-	-	1	μA			
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	16.7	17.7	22	V			
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	-	24	V			
neverse ciamping voitage	at I <sub>PP</sub> = 5 A	VC VC	-	-	33	V			
Capacitance	at $V_R = 0 V$ ; $f = 1 MHz$	$C_D$	-	2.5	5	pF			



<b>ELECTRICAL CHARACTERISTICS GL24T</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) pin 1 to pin 2; pin 3 not connected								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse stand-off voltage		V <sub>RWM</sub>	-	=	24	V		
Reverse voltage	at I <sub>R</sub> = 1 μA	V <sub>R</sub>	24	-	-	V		
Reverse current	at V <sub>R</sub> = 24 V	I <sub>R</sub>	-	=	1	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	26.7	28.2	33	V		
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	=	43	V		
neverse clamping voltage	at I <sub>PP</sub> = 5 A	☐ VC	-	-	55	V		
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	C <sub>D</sub>	-	2.5	5	pF		

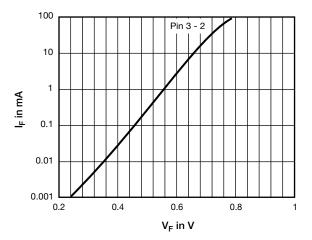


Fig. 1 - Typical Forward Current  $I_F$  vs. Forward Voltage  $V_F$ 

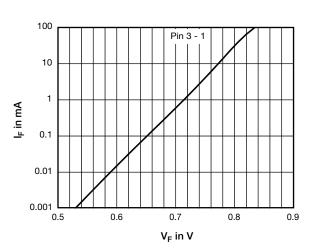


Fig. 2 - Typical Forward Current I<sub>F</sub> vs. Forward Voltage V<sub>F</sub>

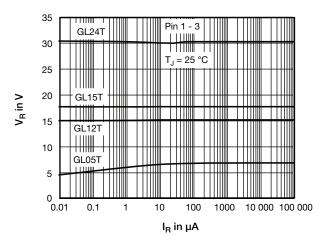
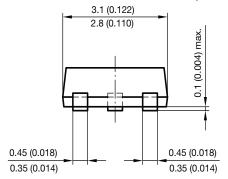
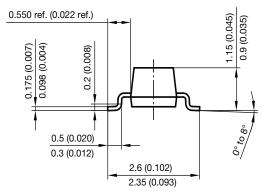


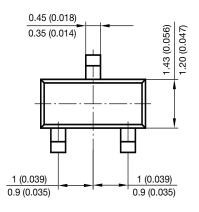
Fig. 3 - Typical Reverse Voltage  $V_{\text{R}}$  vs. Reverse Current  $I_{\text{R}}$ 

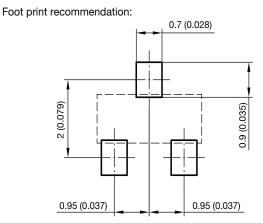


### PACKAGE DIMENSIONS in millimeters (inches): SOT-23



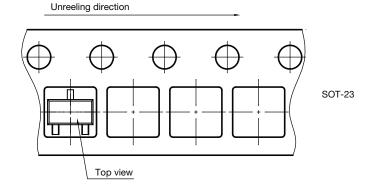






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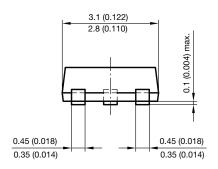
17418

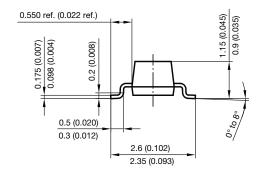


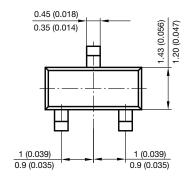
Orientation in carrier tape SOT-23 S8-V-3929.01-006 (4) 04.02.2010 22607



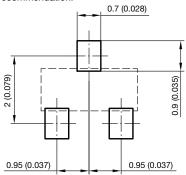
## **PACKAGE DIMENSIONS** in millimeters (inches)







Foot print recommendation:



Document no.: 6.541-5014.01-4 Rev. 8 - Date: 23.Sept.2009

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Revision: 02-Oct-12 Document Number: 91000