

Power Resistor Thick Film Technology



LTO series are the extension of RTO types. We used the direct ceramic mounting design (no metal tab) of our RCH power resistors applied to semiconductor packages.

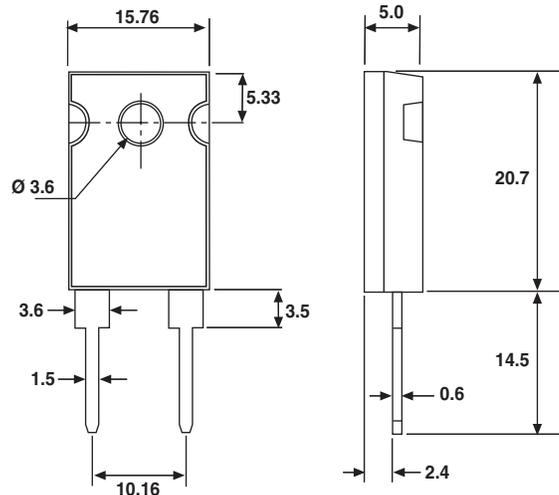
FEATURE

- 100 W at 25 °C case temperature heatsink mounted
- Direct mounting ceramic on heatsink
- Broad resistance range: 0.015 Ω to 1 M Ω
- Non inductive
- TO-247 package: Compact and easy to mount
- Compliant to RoHS directive 2002/95/EC



RoHS
COMPLIANT

DIMENSIONS in millimeters



Note

- Tolerance unless otherwise specified: ± 0.3 mm

MECHANICAL SPECIFICATIONS

Mechanical Protection	Molded
Resistive Element	Thick film
Substrate	Alumina
Connections	Tinned copper
Weight	3.5 g max.
Mounting Torque	1 Nm

DIMENSIONS

Standard Package	TO247 package
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ENVIRONMENTAL SPECIFICATIONS

Temperature Range	- 55 °C to + 175 °C
Climatic Category	55/175/56
Flammability	IEC 60695-11-5 2 applications 30 s separated by 60 s

ELECTRICAL SPECIFICATIONS

Resistance Range	0.015 Ω to 1 M Ω
Tolerances (Standard)	± 1 % to ± 10 %
Dissipation and Associated	Onto a heatsink
Power Rating and Thermal Resistance of the Component	100 W at + 25 °C (case temp.) $R_{TH(j-c)}$: 1.5 °C/W Free air: 3.5 W at + 25 °C
Temperature Coefficient Standard	See Performance table ± 150 ppm/°C
Limiting Element Voltage U_L	375 V
Dielectric Strength MIL STD 202	1500 V _{RMS} - 1 min 10 mA max.
Insulation Resistance	$\geq 10^4$ M Ω
Inductance	≤ 0.1 μ H
Critical Resistance	1.41 k Ω

PERFORMANCE		
TESTS	CONDITIONS	REQUIREMENTS
Momentary Overload	EN 60115-1 1.5 P _r /5 s U _s < 1.5 U _L	± (0.5 % + 0.005 Ω)
Rapid Temperature Change	EN 60115-1 IEC 60068-2-14 Test Na 5 cycles - 55 °C to + 175 °C	± (0.5 % + 0.005 Ω)
Load Life	EN 60115-1 1000 h P _r at + 25 °C	± (1 % + 0.005 Ω)
Humidity (Steady State)	MIL STD 202 Method 103 B Cond. D	± (0.5 % + 0.005 Ω)
Vibration	MIL STD 202 Method 204 Cond. D	± (0.2 % + 0.005 Ω)
Terminal Strength	MIL STD 202 Method 211 Cond. A1	± (0.2 % + 0.005 Ω)
Shock	100G, MIL STD 202 Method 213 Cond. I	± (0.5 % + 0.005 Ω)

SPECIAL FEATURES				
Resistance Values	≥ 0.010	≥ 0.015	≥ 0.1	≥ 0.5
Tolerances	± 1 % at ± 10 %			
Typical Temperature Coefficient (- 55 ° to + 175 °C)	± 900 ppm/°C	± 700 ppm/°C	± 250 ppm/°C	± 150 ppm/°C

CHOICE OF THE HEATSINK

The user must choose according to the working conditions of the component (power, room temperature). Maximum working temperature must not exceed 175 °C. The dissipated power is simply calculated by the following ratio:

$$P = \frac{\Delta T}{[R_{TH(j-c)} + R_{TH(c-a)}]} \quad (1)$$

- P: Expressed in W
- ΔT: Difference between maximum working temperature and room temperature
- R_{TH(j-c)}: Thermal resistance value measured between resistive layer and outer side of the resistor. It is the thermal resistance of the component.
- R_{TH(c-a)}: Thermal resistance value measured between outer side of the resistor and room temperature. It is the thermal resistance of the heatsink itself (type, shape) and the quality of the fastening device, and the thermal resistance of the thermal compound.

Example:

R_{TH(c-a)} for LTO 100 power rating 10 W at ambient temperature + 25 °C

Thermal resistance R_{TH(j-c)}: 1.5 °C/W

Considering equation (1) we have:

$$\Delta T = 175 \text{ °C} - 25 \text{ °C} = 150 \text{ °C}$$

$$R_{TH(j-c)} + R_{TH(c-a)} = \frac{\Delta T}{P} = \frac{150}{10} = 15 \text{ °C/W}$$

$$R_{TH(c-a)} = 15 \text{ °C/W} - 1.5 \text{ °C/W} = 13.5 \text{ °C/W}$$

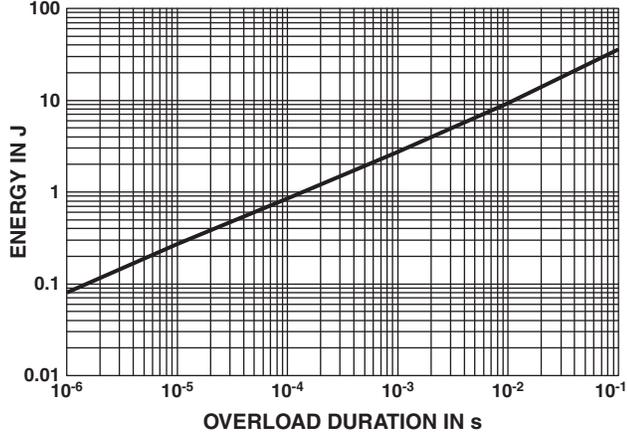
with a thermal grease R_{TH(c-h)} = 1 °C/W, we need a heatsink with R_{TH(h-a)} = 12.5 °C/W.



OVERLOADS

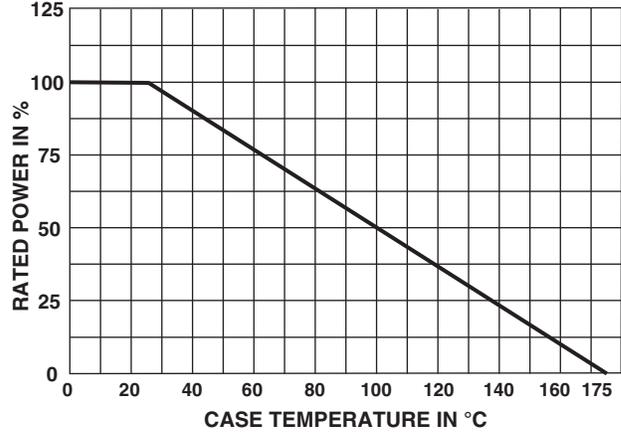
In any case the applied voltage must be lower than the maximum overload voltage of 560 V.
The values indicated on the graph below are applicable to resistors in air or mounted onto a heatsink.

ENERGY CURVE



POWER RATING

The temperature of the case should be maintained within the limits specified.
To improve the thermal conductivity, surfaces in contact should be coated with a silicone grease and the torque applied on the screw for tightening should be around 1 Nm.



MARKING

Model, style, resistance value (in Ω), tolerance (in %), manufacturing date, Vishay Sfernice trademark.

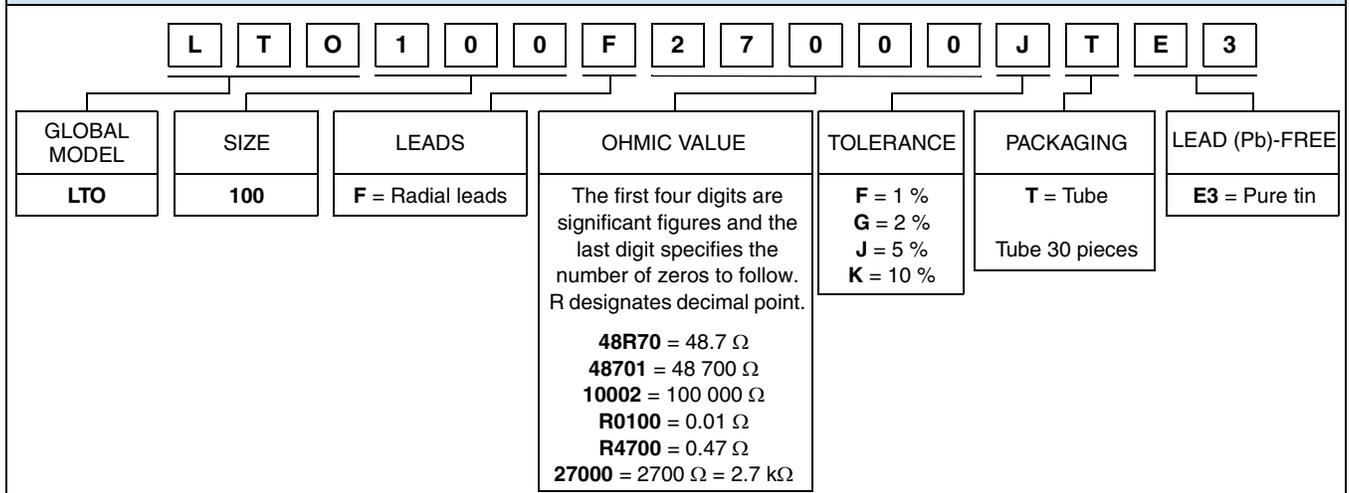
PACKAGING

Tube of 30 units

ORDERING INFORMATION

MODEL	STYLE	CONNECTIONS	RESISTANCE VALUE	TOLERANCE	CUSTOM DESIGN	PACKAGING	LEAD (Pb)-FREE
LTO	100	F	2.7 kΩ	± 1 %	xxx	TU30	e3
				± 1 % ± 2 % ± 5 % ± 10 %	Optional on request: Special TCR, shape etc.		

GLOBAL PART NUMBER INFORMATION





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