Interface and switching (60V, 115mA)

RK7002

●Structure

Silicon N-channel MOSFET

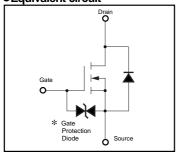
● Features

- 1) Low on-resistance.
- 2) High-speed switching.
- 3) Low-voltage drive(5V).

Application

Switching

●Equivalent circuit



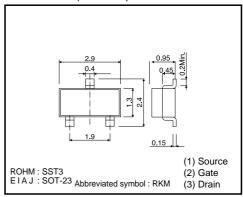
* A protection diode has been built in between the gate and the source to protect against static electricity when the product is in use. Use the protection circuit when fixed voltages are exceeded.

● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		VDSS	60	V
Gate-source voltage		Vgss	±20	V
Drain current	Continuous	lo	115	mA
	Pulsed	IDP*1	800	mA
Reverse drain current	Continuous	Idr	115	mA
	Pulsed	IDRP*1	800	mA
Total power dissipation		Pp*2	225	mW
Channel temperature		Tch	150	°C
Storage temperature		Tstg	-55 to +150	°C

^{*1} Pw≤10μs, Duty cycle≤1%

●Dimensions (Unit:mm)



^{*2} When mounted on a 1x0.75x0.062 inch glass epoxy board.

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conditions
Gate-source leakage	Igss	-	_	±10	μΑ	Vgs= ±20V, Vps= 0V
Drain-source breakdown voltage	V(BR)DSS	60	_	_	V	I _D =10μA, V _G s=0V
Zero gate voltage drain current	Ipss	_	_	1.0	μΑ	V _{DS} = 60V, V _{GS} = 0V
Gate threshold voltage	VGS (th)	1.0	1.85	2.5	V	V _{DS} = 10V, I _D = 1mA
Static drain-source on-state	D *	_	_	7.5	Ω	I _D = 0.5A, V _G s=10V
resistance	RDS(on) *	_	_	7.5		ID= 0.05A, VGS= 5V
Forward transfer admittance	Y _{fs} *	80	_	_	mS	ID= 0.2A, VDS= 10V
Input capacitance	Ciss	_	25	50	pF	V _{DS} = 25V
Output capacitance	Coss	_	10	25	pF	Vgs= 0V
Reverse transfer capacitance	Crss	_	3.0	5.0	pF	f= 1MHz
Turn-on delay time	td(on)*	_	12	20	ns	I _D = 0.2A, V _D D = 30V, V _G S=10V,
Turn-off delay time	td(off)*	-	20	30	ns	R _L =150Ω, R _G =10Ω

^{*} Pw≤300µs, Duty cycle≤1%

•Electrical characteristic curves

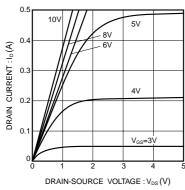


Fig.1 Typical Output Characteristics

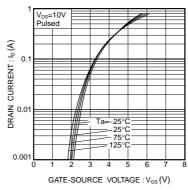


Fig.2 Typical Transfer Characteristics

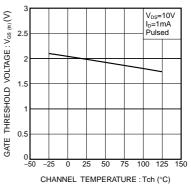


Fig.3 Gate Threshold Voltage vs. Channel Temperature

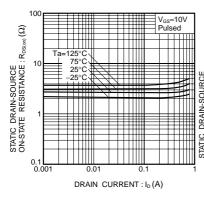


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current(I)

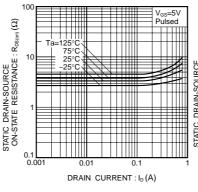


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current (II)

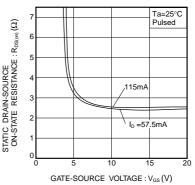


Fig.6 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

Rev.A

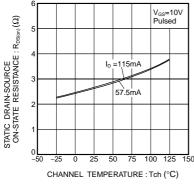


Fig.7 Static Drain-Source On-State Resistance vs. Channel Temperature

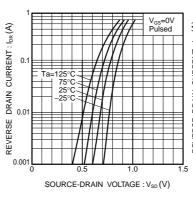


Fig.8 Reverse Drain Current vs. Source-Drain Voltage (I)

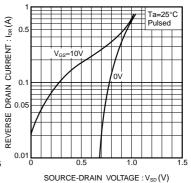


Fig.9 Reverse Drain Current vs. Source-Drain Voltage (II)

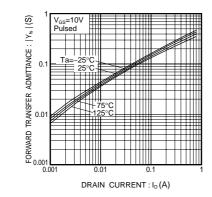


Fig.10 Reverse Drain Current vs. Source-Drain Voltage (II)

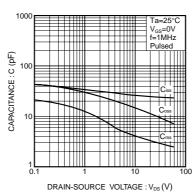


Fig.11 Typical Capacitance vs. Drain-Source Voltage

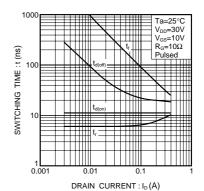


Fig.12 Switching Characteristics (See Figure. 13 and 14 for measurement circuits)

Measurement circuit

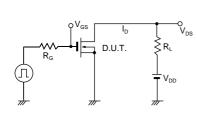


Fig.13 Switching Time Test Circuit

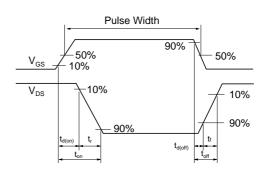


Fig.14 Switching Time Waveforms

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