SD103AW-V, SD103BW-V, SD103CW-V



Vishay Semiconductors

Small Signal Schottky Diodes

Features

- The low forward voltage drop and fast switching make it ideal for protection of MOS devices, steering, biasing, and coupling diodes for fast switching and low logic level applications.
- (e3)
- Other applications are click suppression, efficient full wave bridges in telephone subsets, and blocking diodes in rechargeable low voltage battery systems.



- The SD103 series is a metal-on-silicon Schottky barrier device which is protected by a PN junction guard ring.
- This diode is also available in the MiniMELF case with the type designations LL103A to LL103C, DO35 case with the type designations SD103A to SD103C and SOD323 case with type designations SD103AWS-V to SD103CWS-V.
- For general purpose applications
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC

Mechanical Data

Case: SOD123 plastic case Weight: approx. 10.3 mg Packaging codes/options:

GS18/10 k per 13" reel (8 mm tape), 10 k/box GS08/3 k per 7" reel (8 mm tape), 15 k/box

Parts Table

Part	Ordering code	Type marking	Remarks
SD103AW-V	SD103AW-V-GS18 or SD103AW-V-GS08	S6	Tape and reel
SD103BW-V	SD103BW-V-GS18 or SD103BW-V-GS08	S7	Tape and reel
SD103CW-V	SD103CW-V-GS18 or SD103CW-V-GS08	S8	Tape and reel

Absolute Maximum Ratings

T_{amb} = 25 °C, unless otherwise specified

anno					
Parameter	Test condition	Part	Symbol	Value	Unit
		SD103AW-V	V_{RRM}	40	V
Peak reverse voltage		SD103BW-V	V_{RRM}	30	V
		SD103CW-V	V_{RRM}	20	V
Power dissipation (Infinite heat sink)			P _{tot}	400 ¹⁾	mW
Single cycle surge	10 μs square wave		I _{FSM}	2	Α

Note

Thermal Characteristics

T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit	
Thermal resistance junction to ambient air		R_{thJA}	300 ¹⁾	K/W	
Junction temperature		Tj	125	°C	
Storage temperature range		T _{stg}	- 55 to + 150	°C	

Note

¹⁾ Valid provided that electrodes are kept at ambient temperature

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Electrical Characteristics

T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Part	Symbol	Min.	Тур.	Max.	Unit
Leakage current	V _R = 30 V	SD103AW-V	I _R			5	μΑ
	V _R = 20 V	SD103BW-V	I _R			5	μΑ
	V _R = 10 V	SD103CW-V	I _R			5	μΑ
Forward voltage drop	I _F = 20 mA		V_{F}			370	mV
	I _F = 200 mA		V_{F}			600	mV
Diode capacitance	V _R = 0 V, f = 1 MHz		C _D		50		pF
Reverse recovery time	$I_F = I_R = 50 \text{ mA to } 200 \text{ mA},$ recover to 0.1 I_R		t _{rr}		10		ns

Typical Characteristics

T_{amb} = 25 °C, unless otherwise specified

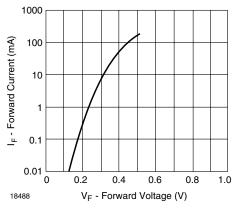


Figure 1. Typical Variation of Forward Current vs. Forward Voltage

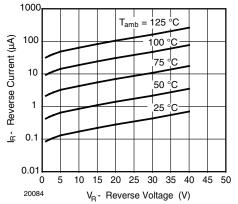


Figure 3. Typical Variation of Reverse Current at Various Temperatures

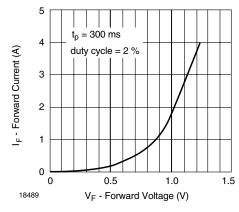


Figure 2. Typical High Current Forward Conduction Curve

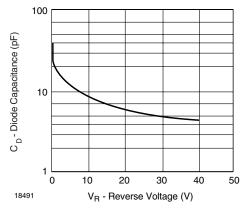


Figure 4. Typical Capacitance vs. Reverse Voltage

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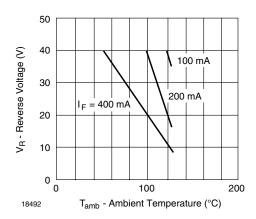
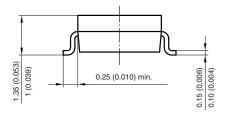
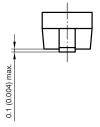
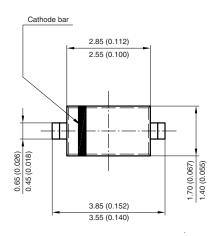


Figure 5. Blocking Voltage Deration vs. Temperature at Various Average Forward Currents

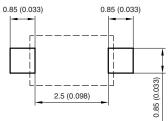
Package Dimensions in mm (inches): SOD123







Foot print recommendation:



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