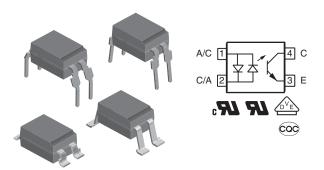


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Vishay Semiconductors

Optocoupler, Phototransistor Output, AC Input

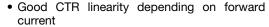


DESCRIPTION

The SFH620A (DIP) and SFH6206 (SMD) feature a high current transfer ratio, low coupling capacitance and high isolation voltage. These couplers have a GaAs infrared diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a plastic DIP-4 or SMD package.

The coupling devices are designed for signal transmission between two electrically separated circuits. The couplers are end-stackable with 2.54 mm lead spacing. Creepage and clearance distances of > 8 mm are achieved with option 6. This version complies with IEC 60950 (DIN VDE 0805) for reinforced insulation to an operation voltage of 400 $V_{\rm RMS}$ or DC.

FEATURES



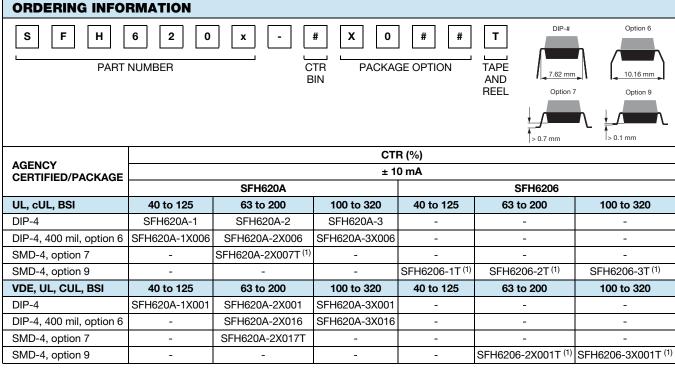


- Isolation test voltage, 5300 V_{RMS}
- High collector emitter voltage, V_{CEO} = 70 V
- Low saturation voltage
- Fast switching times
- Low CTR degradation
- Temperature stable
- Low coupling capacitance
- End-stackable, 0.100" (2.54 mm) spacing
- High common-mode interference immunity
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

AGENCY APPROVALS

The safety application model number covering all products in this datasheet is SFH620A and SHF6206. This model number should be used when consulting safety agency documents.

- UL1577, file no. E52744 system code H, double protection
- CSA 93751
- BSI EN 60950, EN 60065
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- CQC GB8898-2011, GB4943.1-2011



Notes

- Additional options may be possible, please contact sales office.
- (1) Also available in tubes; do not add T to end.



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
DC forward current		I _F	± 60	mA			
Surge forward current	t _p ≤ 10 μs	I _{FSM}	± 2.5	Α			
Power dissipation		P _{diss}	100	mW			
OUTPUT							
Collector emitter voltage		V_{CEO}	70	V			
Emitter collector voltage		V_{ECO}	7	V			
Collector current		I _C	50	mA			
Collector current	t _p ≤ 1 µs	I _C	100	mA			
Power dissipation		P _{diss}	150	mW			
COUPLER							
Total power dissipation		P _{tot}	250	mW			
Storage temperature range		T _{stg}	-55 to +150	°C			
Ambient temperature range		T _{amb}	-55 to +100	°C			
Junction temperature		Tj	100	°C			
Soldering temperature (1)	max. 10 s, dip soldering distance to seating plane ≥ 1.5 mm	T _{sld}	260	°C			

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT	INPUT							
Forward voltage	$I_F = \pm 60 \text{ mA}$		V_{F}		1.25	1.65	V	
Capacitance	$V_R = 0 V, f = 1 MHz$		Co		50		pF	
Thermal resistance			R _{thja}		750		K/W	
OUTPUT								
Collector emitter capacitance	V _{CE} = 5 V, f = 1 MHz		C _{CE}		6.8		pF	
Thermal resistance			R _{thja}		500		°C/W	
COUPLER								
Collector emitter saturation voltage	$I_F = \pm 10 \text{ mA}, I_C = 2.5 \text{ mA}$		V _{CEsat}		0.25	0.4	V	
Coupling capacitance			C _C		0.2		pF	
Collector emitter leakage current	V _{CE} = 10 V	SFH620A-1	I _{CEO}		2	50	nA	
		SFH6206-1	I _{CEO}		2	50	nA	
		SFH620A-2	I _{CEO}		2	50	nA	
		SFH6206-2	I _{CEO}		2	50	nA	
		SFH620A-3	I _{CEO}		5	100	nA	
		SFH6206-3	I _{CEO}		5	100	nA	

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.
 Still air, coupler soldered to PCB or base.

CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
	$V_{CE} = 5 \text{ V}, I_F = \pm 10 \text{ mA}$	SFH620A-1	CTR	40		125	%
		SFH6206-1	CTR	40		125	%
		SFH620A-2	CTR	63		200	%
		SFH6206-2	CTR	63		200	%
		SFH620A-3	CTR	100		320	%
		SFH6206-3	CTR	100		320	%
I _C /I _F	V _{CE} = 5 V, I _F = ± 1 mA	SFH620A-1	CTR	13	30		%
$V_{CE} = 5 \text{ V}, I_{F}$		SFH6206-1	CTR	13	30		%
		SFH620A-2	CTR	22	45		%
		SFH6206-2	CTR	22	45		%
		SFH620A-3	CTR	34	70		%
		SFH6206-3	CTR	34	70		%

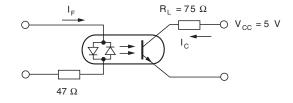


Fig. 1 - Switching Times Linear Operation (without Saturation)

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SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$R_L = 75~\Omega,~I_F = \pm~10~mA,~V_{CC} = 5~V$	t _{on}		3		μs
Rise time	$R_L = 75 \Omega$, $I_F = \pm 10 \text{ mA}$, $V_{CC} = 5 \text{ V}$	t _r		2		μs
Turn-off time	$R_L = 75 \Omega$, $I_F = \pm 10 \text{ mA}$, $V_{CC} = 5 \text{ V}$	t _{off}		2.3		μs
Fall time	$R_L = 75 \Omega$, $I_F = \pm 10 \text{ mA}$, $V_{CC} = 5 \text{ V}$	t _f		2		μs
Cut-off frequency	$R_L = 75~\Omega,~I_F = \pm~10~mA,~V_{CC} = 5~V$	t _{ctr}		208		kHz

SAFETY AND INSULATION RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification (according to IEC 68 part 1)			55/100/21	
Comparative tracking index		CTI	175	
Maximum rated withstanding isolation voltage	t = 1 min	V _{ISO}	4420	V _{RMS}
Maximum transient isolation voltage		V _{IOTM}	10 000	V
Maximum repetitive peak isolation voltage		V_{IORM}	890	V
Isolation resistance	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω
Isolation resistance	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω
Output safety power		P _{SO}	400	mW
Input safety current		I _{SI}	275	mA
Input safety temperature		T _{SI}	175	°C
Creepage distance	Standard DIP-4		≥ 7	mm
Clearance distance	Standard DIP-4		≥ 7	mm
Creepage distance	400 mil DIP-4		≥ 8	mm
Clearance distance	400 mil DIP-4		≥ 8	mm
Insulation thickness		DTI	≥ 0.4	mm

Note

As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits.

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

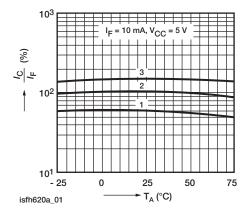


Fig. 2 - Current Transfer Ratio (CTR) vs. Temperature

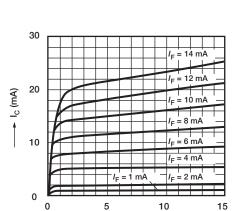


Fig. 3 - Output Characteristics (Typ.) Collector Current vs. Collector Emitter Voltage

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 $V_{CE}(V)$

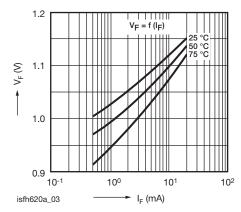


Fig. 4 - Diode Forward Voltage (Typ.) vs. Forward Current

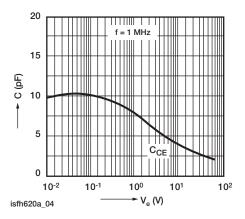


Fig. 5 - Transistor Capacitance (Typ.) vs. Collector Emitter Voltage

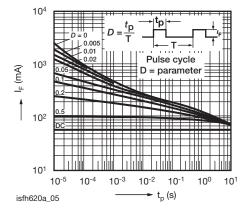


Fig. 6 - Permissible Pulse Handling Capability Forward Current vs. Pulse Width

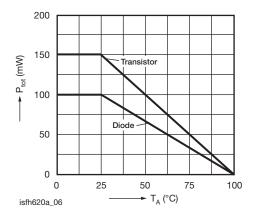


Fig. 7 - Permissible Power Dissipation vs. Ambient Temperature

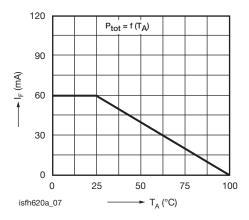
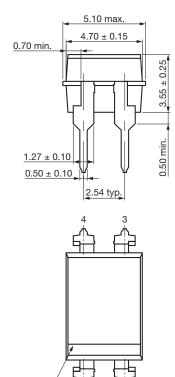


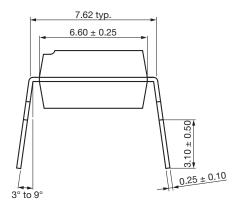
Fig. 8 - Permissible Diode Forward Current vs. Ambient Temperature

PACKAGE DIMENSIONS in millimeters

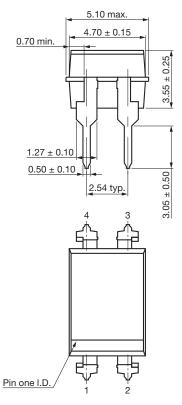
DIP-4, Standard

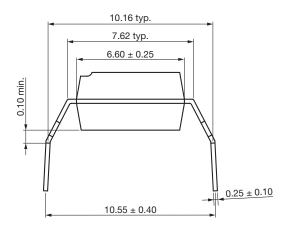


Pin one I.D.

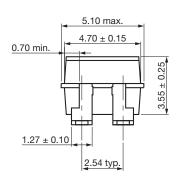


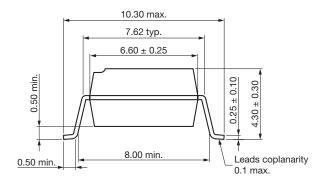
DIP-4, Option 6

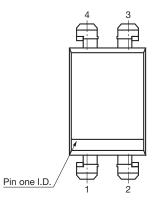


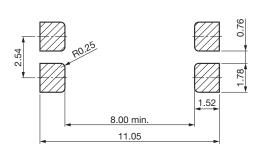


SMD-4, Option 7



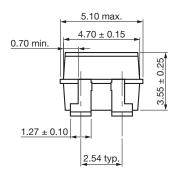


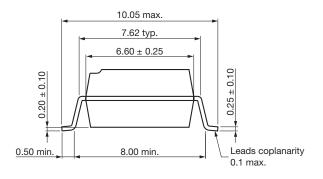


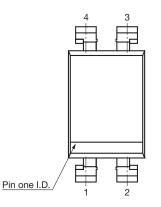


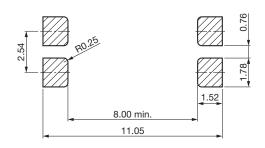


SMD-4, Option 9

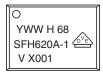








PACKAGE MARKING (example)



Notes

- Only options 1 and 7 are reflected in the package marking.
- The VDE logo is only marked on option1 parts.
- Tape and reel suffix (T) is not part of the package marking.



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