



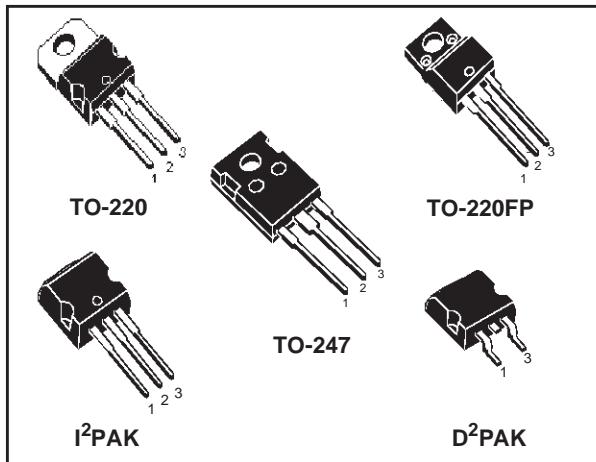
# STP13NK60Z/FP, STB13NK60Z STB13NK60Z-1, STW13NK60Z

N-CHANNEL600V-0.48Ω-13ATO-220/FP/D<sup>2</sup>PAK/I<sup>2</sup>PAK/TO-247

Zener-Protected SuperMESH™ Power MOSFET

TYPE	V <sub>DSS</sub>	R <sub>DS(on)</sub>	I <sub>D</sub>	P <sub>w</sub>
STP13NK60Z	600 V	< 0.55 Ω	13 A	135 W
STP13NK60ZFP	600 V	< 0.55 Ω	13 A	40 W
STB13NK60Z	600 V	< 0.55 Ω	13 A	135 W
STB13NK60Z-1	600 V	< 0.55 Ω	13 A	135 W
STW13NK60Z	600 V	< 0.55 Ω	13 A	160 W

- TYPICAL R<sub>DS(on)</sub> = 0.48 Ω
- EXTREMELY HIGH dv/dt CAPABILITY
- 100% AVALANCHE TESTED
- GATE CHARGE MINIMIZED
- VERY LOW INTRINSIC CAPACITANCES
- VERY GOOD MANUFACTURING REPEATABILITY



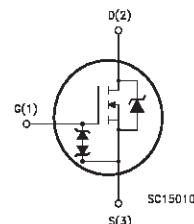
## DESCRIPTION

The SuperMESH™ series is obtained through an extreme optimization of ST's well established strip-based PowerMESH™ layout. In addition to pushing on-resistance significantly down, special care is taken to ensure a very good dv/dt capability for the most demanding applications. Such series complements ST full range of high voltage MOSFETs including revolutionary MDmesh™ products.

## APPLICATIONS

- HIGH CURRENT, HIGH SPEED SWITCHING
- IDEAL FOR OFF-LINE POWER SUPPLIES, ADAPTORS AND PFC
- LIGHTING

## INTERNAL SCHEMATIC DIAGRAM



## ORDERING INFORMATION

SALES TYPE	MARKING	PACKAGE	PACKAGING
STP13NK60Z	P13NK60Z	TO-220	TUBE
STP13NK60ZFP	P13NK60ZFP	TO-220FP	TUBE
STB13NK60ZT4	B13NK60Z	D <sup>2</sup> PAK	TAPE & REEL
STB13NK60Z	B13NK60Z	D <sup>2</sup> PAK	TUBE (ONLY UNDER REQUEST)
STB13NK60Z-1	B13NK60Z	I <sup>2</sup> PAK	TUBE
STW13NK60Z	W13NK60Z	TO-247	TUBE

## STP13NK60Z, STP13NK60ZFP, STB13NK60Z, STB13NK60Z-1, STW13NK60Z

### ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value			Unit
		STP13NK60Z STB13NK60Z-1	STP13NK60ZFP	STW13NK60Z	
V <sub>DS</sub>	Drain-source Voltage (V <sub>GS</sub> = 0)	600			V
V <sub>DGR</sub>	Drain-gate Voltage (R <sub>GS</sub> = 20 kΩ)	600			V
V <sub>GS</sub>	Gate- source Voltage	± 30			V
I <sub>D</sub>	Drain Current (continuos) at T <sub>C</sub> = 25°C	13	13 (*)	13	A
I <sub>D</sub>	Drain Current (continuos) at T <sub>C</sub> = 100°C	8	8 (*)	8	A
I <sub>DM</sub> (i)	Drain Current (pulsed)	40	40 (*)	40	A
P <sub>TOT</sub>	Total Dissipation at T <sub>C</sub> = 25°C	135	40	160	W
	Derating Factor	1.07	0.32	1.28	W/°C
V <sub>ESD(G-S)</sub>	Gate source ESD(HBM-C=100pF, R=1.5KΩ)	4000			V
dv/dt (1)	Peak Diode Recovery voltage slope	4.5			V/ns
V <sub>ISO</sub>	Insulation Withstand Voltage (DC)	-	2500	-	V
T <sub>j</sub> T <sub>stg</sub>	Operating Junction Temperature Storage Temperature		-55 to 150 -55 to 150		°C °C

(i) Pulse width limited by safe operating area

(1) I<sub>SD</sub> ≤ 10A, di/dt ≤ 200A/μs, V<sub>DD</sub> ≤ V<sub>(BR)DSS</sub>, T<sub>j</sub> ≤ T<sub>JMAX</sub>.

(\*) Limited only by maximum temperature allowed

### THERMAL DATA

		TO-220 I <sup>2</sup> PAK	D <sup>2</sup> PAK	TO-220FP	TO-247	
Rthj-case	Thermal Resistance Junction-case Max	0.93		3.13	0.78	°C/W
Rthj-pcb	Thermal Resistance Junction-pcb Max (#)		60			°C/W
Rthj-amb	Thermal Resistance Junction-ambient Max			62.5	50	°C/W
T <sub>l</sub>	Maximum Lead Temperature For Soldering Purpose			300		°C

### AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I <sub>AR</sub>	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T <sub>j</sub> max)	10	A
E <sub>AS</sub>	Single Pulse Avalanche Energy (starting T <sub>j</sub> = 25 °C, I <sub>D</sub> = I <sub>AR</sub> , V <sub>DD</sub> = 50 V)	400	mJ

### GATE-SOURCE ZENER DIODE

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
BV <sub>GSO</sub>	Gate-Source Breakdown Voltage	I <sub>GS</sub> =± 1mA (Open Drain)	30			V

(#) When mounted on minimum Footprint

### PROTECTION FEATURES OF GATE-TO-SOURCE ZENER DIODES

The built-in back-to-back Zener diodes have specifically been designed to enhance not only the device's ESD capability, but also to make them safely absorb possible voltage transients that may occasionally be applied from gate to source. In this respect the Zener voltage is appropriate to achieve an efficient and cost-effective intervention to protect the device's integrity. These integrated Zener diodes thus avoid the usage of external components.

## STP13NK60Z, STP13NK60ZFP, STB13NK60Z, STB13NK60Z-1, STW13NK60Z

### ELECTRICAL CHARACTERISTICS (TCASE =25°C UNLESS OTHERWISE SPECIFIED) ON/OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V(BR)DSS	Drain-source Breakdown Voltage	I <sub>D</sub> = 250 µA, V <sub>GS</sub> = 0	600			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current (V <sub>GS</sub> = 0)	V <sub>DS</sub> = Max Rating V <sub>DS</sub> = Max Rating, T <sub>C</sub> = 125 °C			1 50	µA µA
I <sub>GSS</sub>	Gate-body Leakage Current (V <sub>DS</sub> = 0)	V <sub>GS</sub> = ± 20V			±10	µA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 100µA	3	3.75	4.5	V
R <sub>DSD(on)</sub>	Static Drain-source On Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 5 A		0.48	0.55	Ω

### DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
g <sub>f</sub> (1)	Forward Transconductance	V <sub>DS</sub> = 8 V, I <sub>D</sub> = 5 A		11		S
C <sub>iss</sub> C <sub>oss</sub> C <sub>rss</sub>	Input Capacitance Output Capacitance Reverse Transfer Capacitance	V <sub>DS</sub> = 25V, f = 1 MHz, V <sub>GS</sub> = 0		2030 210 48		pF pF pF
C <sub>oss eq. (3)</sub>	Equivalent Output Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V to 480V		125		pF

### SWITCHING ON

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
t <sub>d(on)</sub> t <sub>r</sub>	Turn-on Delay Time Rise Time	V <sub>DD</sub> = 300 V, I <sub>D</sub> = 5 A R <sub>G</sub> = 4.7Ω V <sub>GS</sub> = 10 V (Resistive Load see, Figure 3)		22 14		ns ns
Q <sub>g</sub> Q <sub>gs</sub> Q <sub>gd</sub>	Total Gate Charge Gate-Source Charge Gate-Drain Charge	V <sub>DD</sub> = 480V, I <sub>D</sub> = 10 A, V <sub>GS</sub> = 10V		66 11 33	92	nC nC nC

### SWITCHING OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
t <sub>d(off)</sub> t <sub>f</sub>	Turn-off Delay Time Fall Time	V <sub>DD</sub> = 300 V, I <sub>D</sub> = 5 A R <sub>G</sub> = 4.7Ω V <sub>GS</sub> = 10 V (Resistive Load see, Figure 3)		61 12		ns ns
t <sub>r(Voff)</sub> t <sub>f</sub> t <sub>c</sub>	Off-voltage Rise Time Fall Time Cross-over Time	V <sub>DD</sub> = 480V, I <sub>D</sub> = 10 A, R <sub>G</sub> = 4.7Ω, V <sub>GS</sub> = 10V (Inductive Load see, Figure 5)		10 9 20		ns ns ns

### SOURCE DRAIN DIODE

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I <sub>SD</sub> I <sub>SDM (2)</sub>	Source-drain Current Source-drain Current (pulsed)				10 40	A A
V <sub>SD (1)</sub>	Forward On Voltage	I <sub>SD</sub> = 10 A, V <sub>GS</sub> = 0			1.6	V
t <sub>rr</sub> Q <sub>rr</sub> I <sub>RRM</sub>	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current	I <sub>SD</sub> = 10 A, di/dt = 100A/µs V <sub>DD</sub> = 35V, T <sub>j</sub> = 150°C (see test circuit, Figure 5)		570 4.5 16		ns µC A

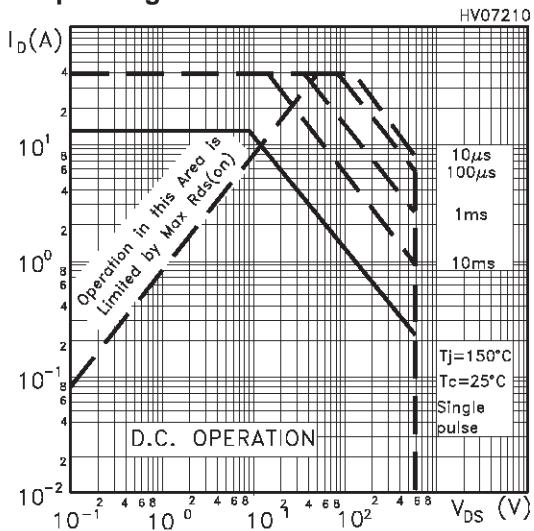
Note: 1. Pulsed: Pulse duration = 300 µs, duty cycle 1.5 %.

2. Pulse width limited by safe operating area.

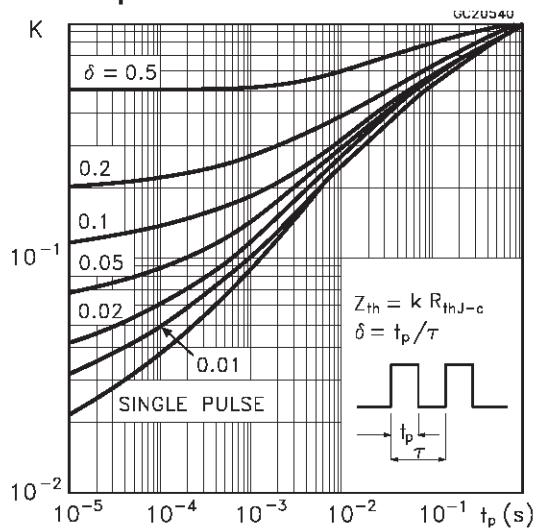
3. C<sub>oss eq.</sub> is defined as a constant equivalent capacitance giving the same charging time as C<sub>oss</sub> when V<sub>DS</sub> increases from 0 to 80% V<sub>DSS</sub>.

## STP13NK60Z, STP13NK60ZFP, STB13NK60Z, STB13NK60Z-1, STW13NK60Z

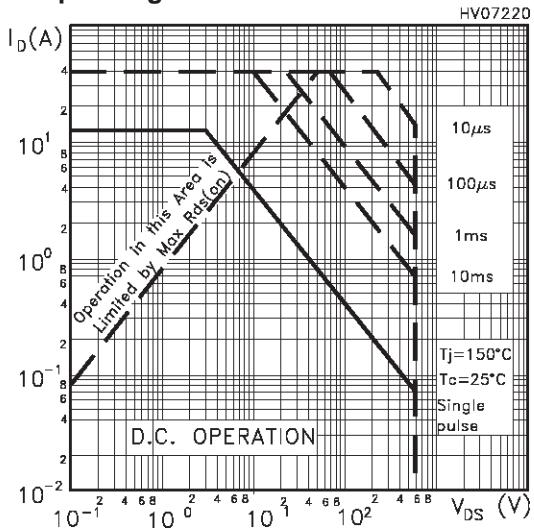
### Safe Operating Area For TO-220/D2PAK/I2PAK



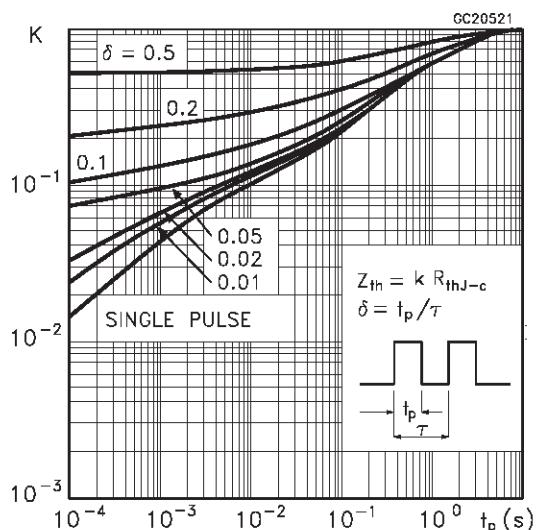
### Thermal Impedance For TO-220/D2PAK/I2PAK



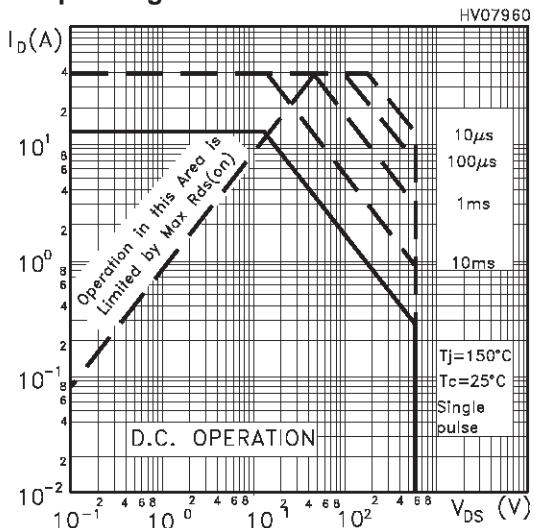
### Safe Operating Area For TO-220FP



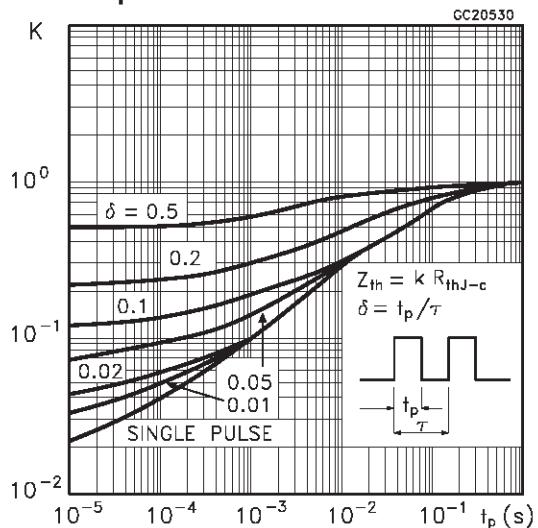
### Thermal Impedance For TO-220FP



### Safe Operating Area For TO-247

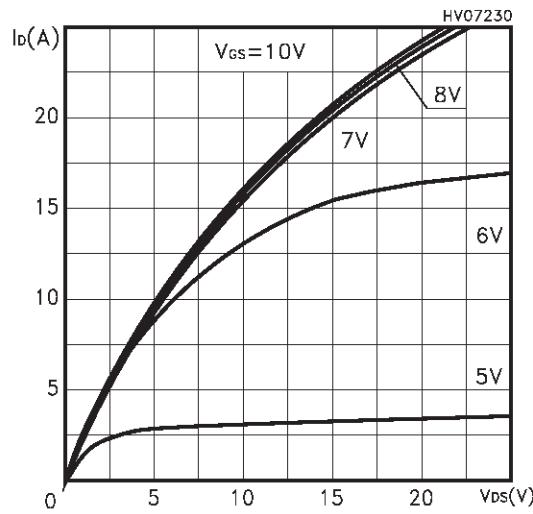


### Thermal Impedance For TO-247

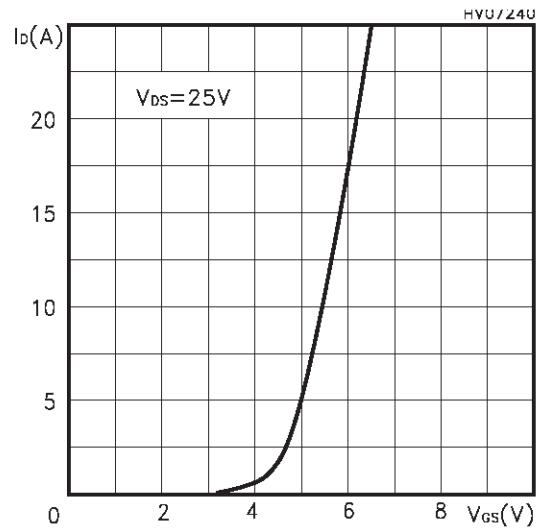


## STP13NK60Z, STP13NK60ZFP, STB13NK60Z, STB13NK60Z-1, STW13NK60Z

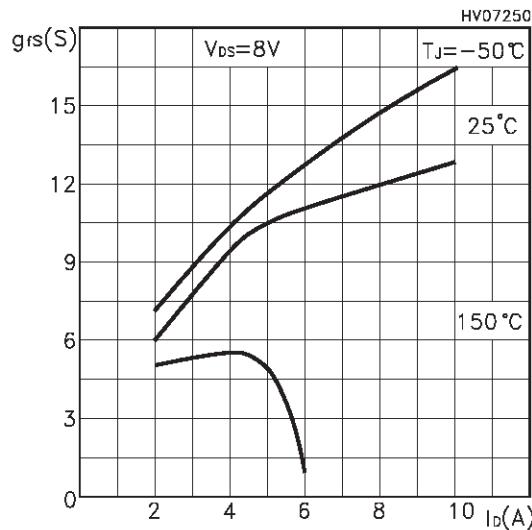
### Output Characteristics



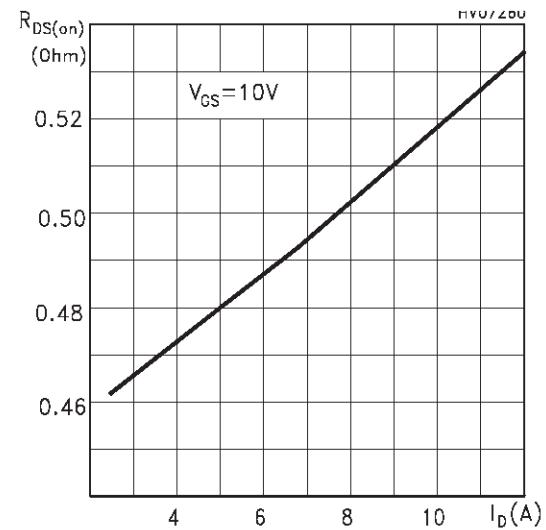
### Transfer Characteristics



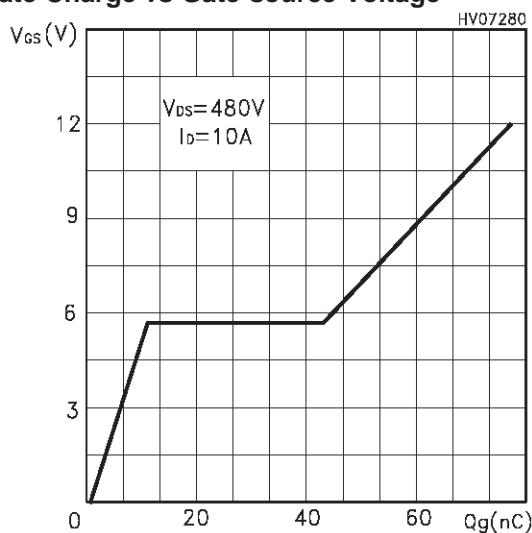
### Transconductance



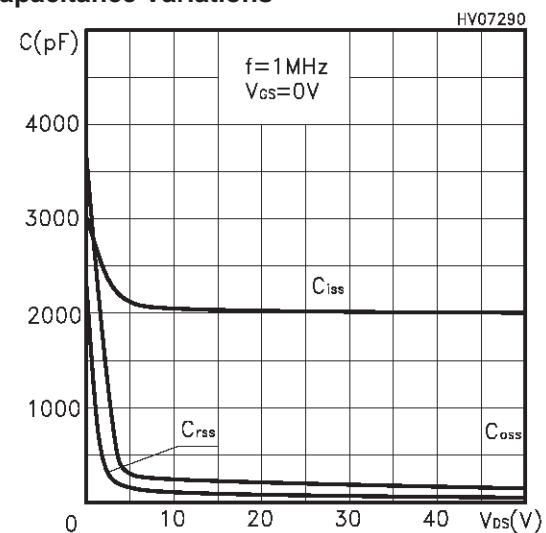
### Static Drain-source On Resistance



### Gate Charge vs Gate-source Voltage

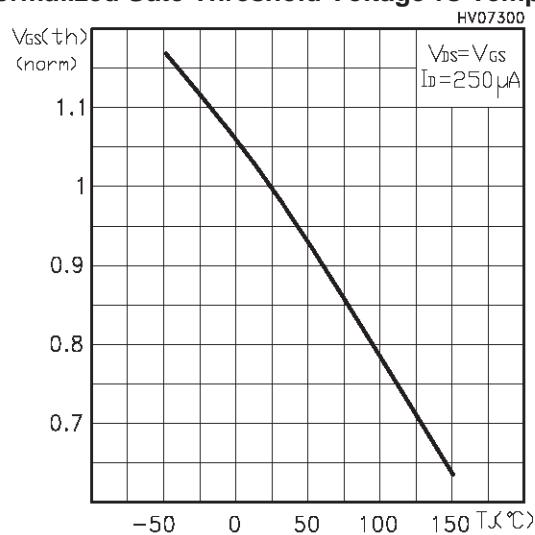


### Capacitance Variations

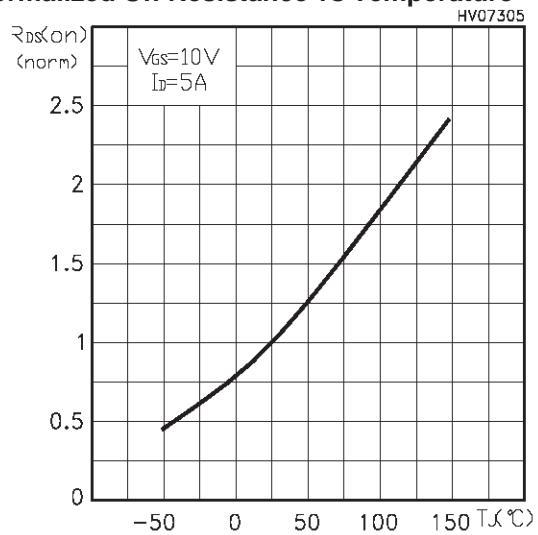


## STP13NK60Z, STP13NK60ZFP, STB13NK60Z, STB13NK60Z-1, STW13NK60Z

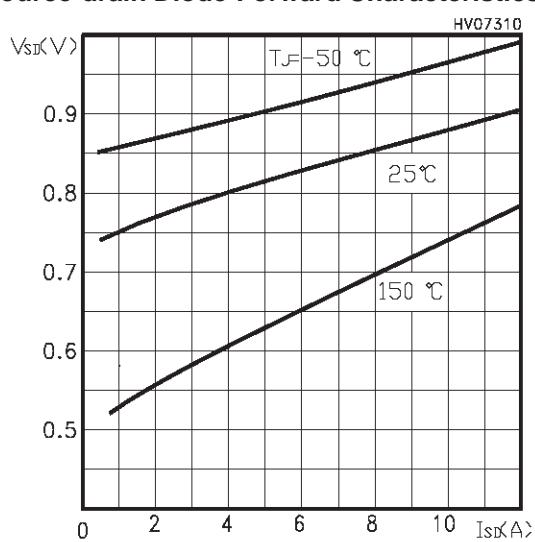
**Normalized Gate Threshold Voltage vs Temp.**



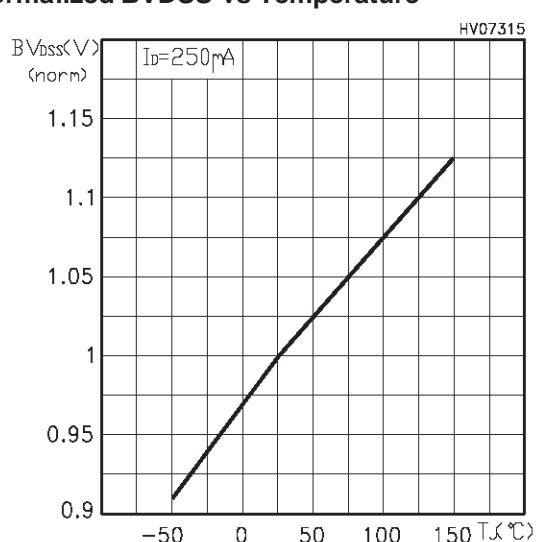
**Normalized On Resistance vs Temperature**



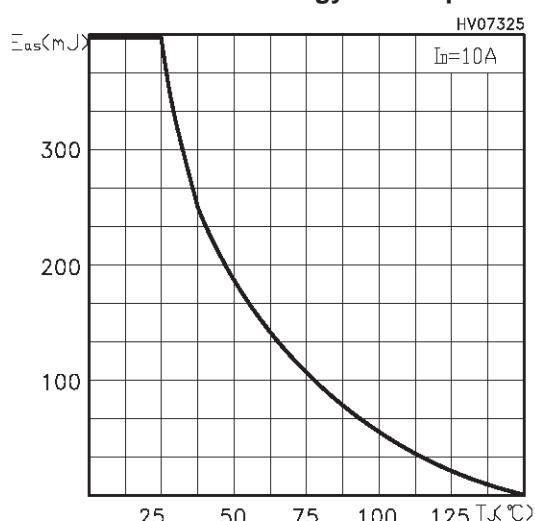
**Source-drain Diode Forward Characteristics**



**Normalized BVDSS vs Temperature**

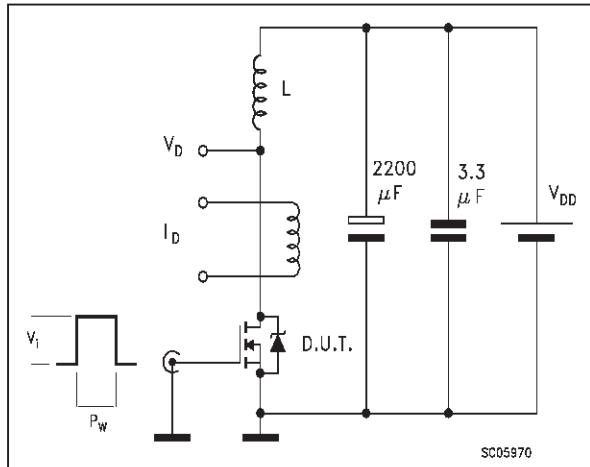


**Maximum Avalanche Energy vs Temperature**

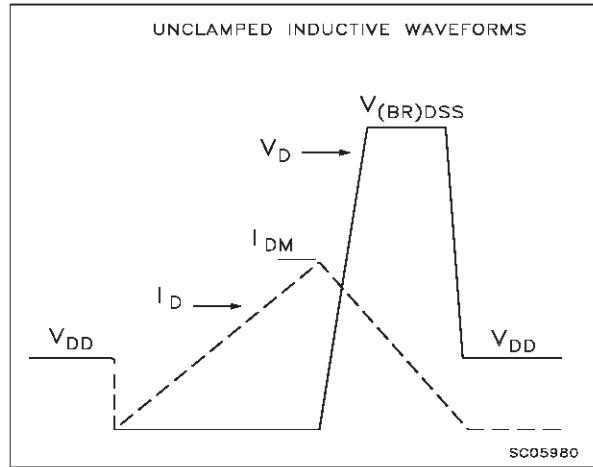


## STP13NK60Z, STP13NK60ZFP, STB13NK60Z, STB13NK60Z-1, STW13NK60Z

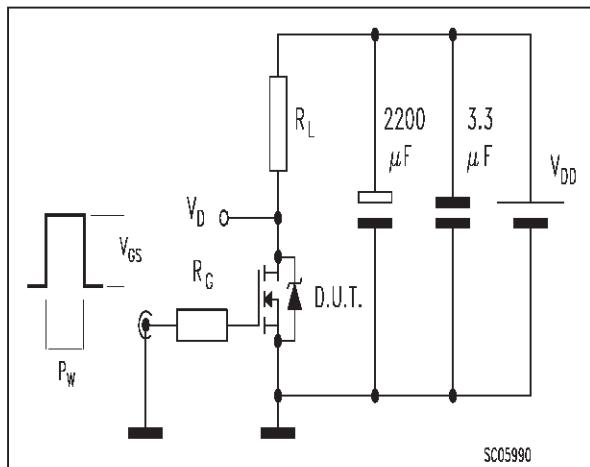
**Fig. 1:** Unclamped Inductive Load Test Circuit



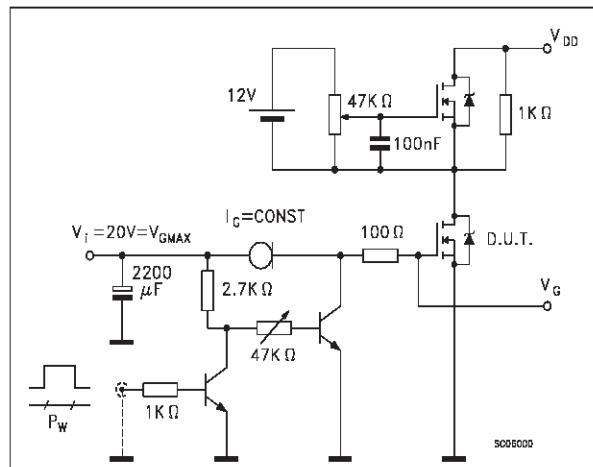
**Fig. 2:** Unclamped Inductive Waveform



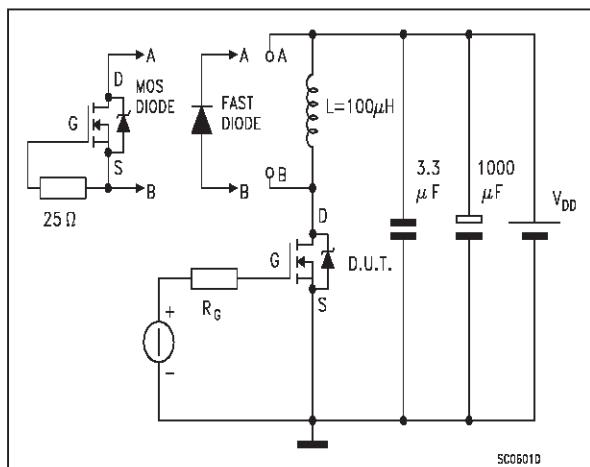
**Fig. 3:** Switching Times Test Circuit For Resistive Load



**Fig. 4:** Gate Charge test Circuit

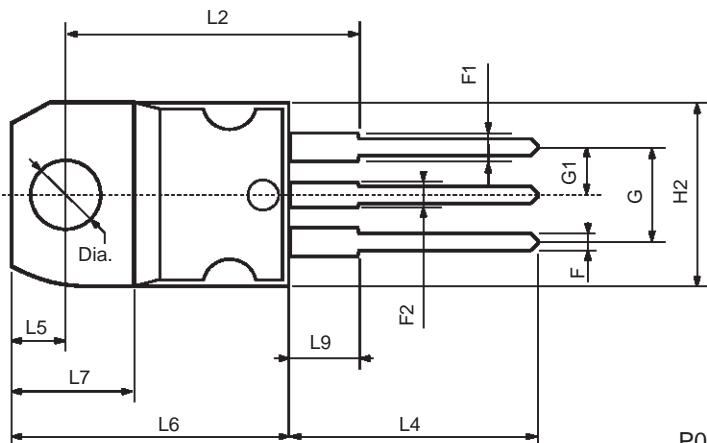
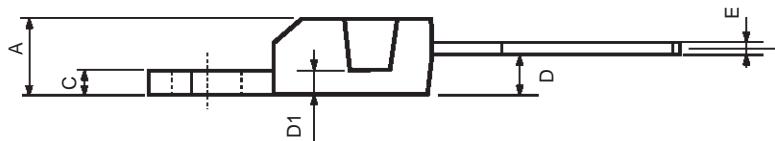


**Fig. 5:** Test Circuit For Inductive Load Switching And Diode Recovery Times



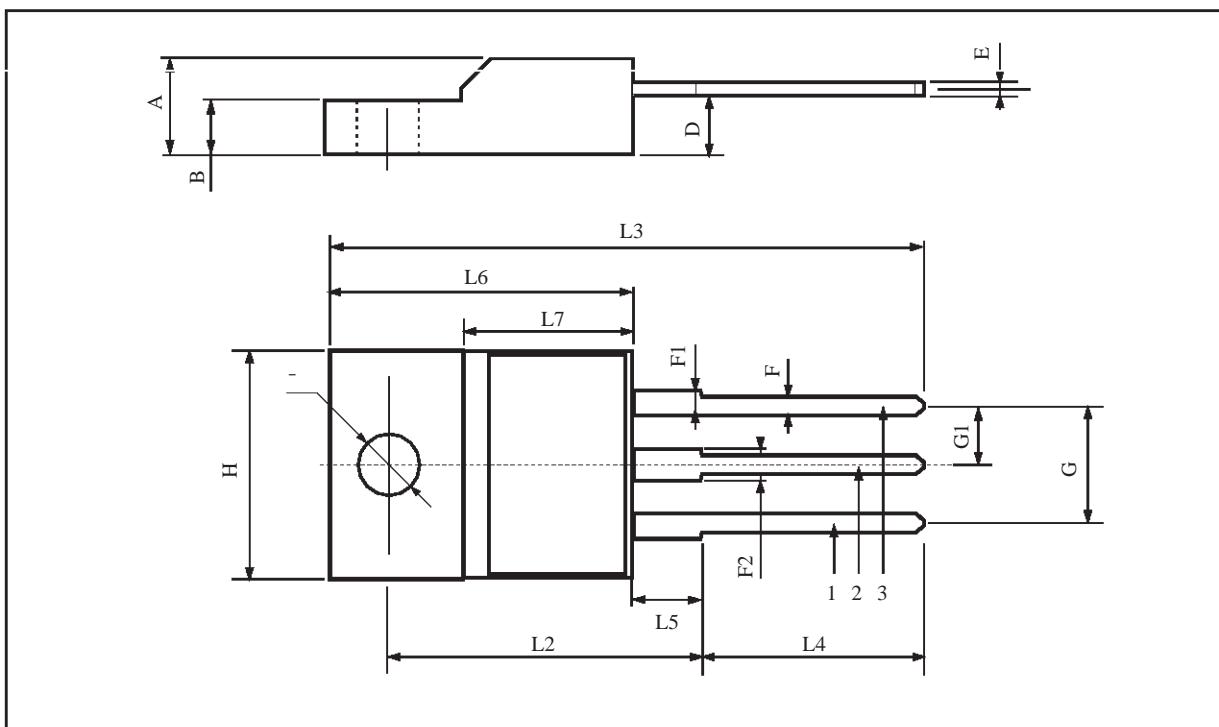
**TO-220 MECHANICAL DATA**

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.40		4.60	0.173		0.181
C	1.23		1.32	0.048		0.051
D	2.40		2.72	0.094		0.107
D1		1.27			0.050	
E	0.49		0.70	0.019		0.027
F	0.61		0.88	0.024		0.034
F1	1.14		1.70	0.044		0.067
F2	1.14		1.70	0.044		0.067
G	4.95		5.15	0.194		0.203
G1	2.4		2.7	0.094		0.106
H2	10.0		10.40	0.393		0.409
L2		16.4			0.645	
L4	13.0		14.0	0.511		0.551
L5	2.65		2.95	0.104		0.116
L6	15.25		15.75	0.600		0.620
L7	6.2		6.6	0.244		0.260
L9	3.5		3.93	0.137		0.154
DIA.	3.75		3.85	0.147		0.151



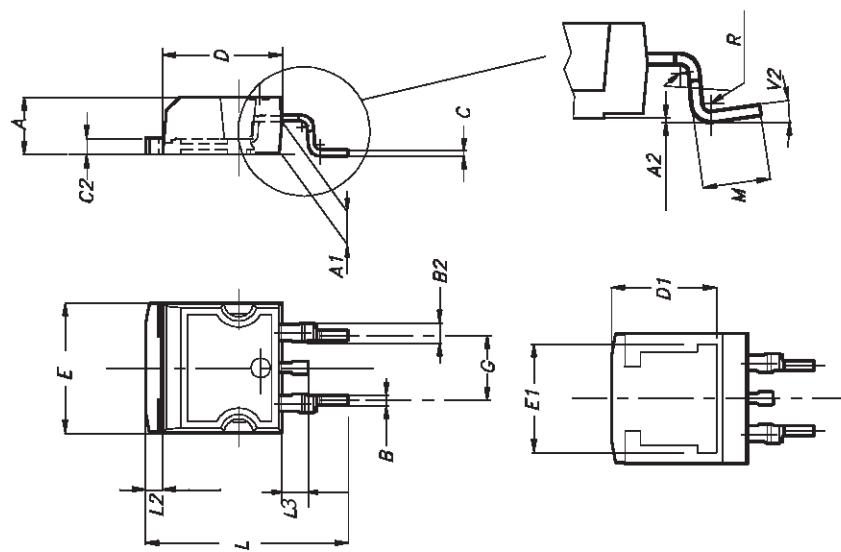
**TO-220FP MECHANICAL DATA**

DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.4		4.6	0.173		0.181
B	2.5		2.7	0.098		0.106
D	2.5		2.75	0.098		0.108
E	0.45		0.7	0.017		0.027
F	0.75		1	0.030		0.039
F1	1.15		1.5	0.045		0.067
F2	1.15		1.5	0.045		0.067
G	4.95		5.2	0.195		0.204
G1	2.4		2.7	0.094		0.106
H	10		10.4	0.393		0.409
L2		16			0.630	
L3	28.6		30.6	1.126		1.204
L4	9.8		10.6	.0385		0.417
L5	2.9		3.6	0.114		0.141
L6	15.9		16.4	0.626		0.645
L7	9		9.3	0.354		0.366
Ø	3		3.2	0.118		0.126



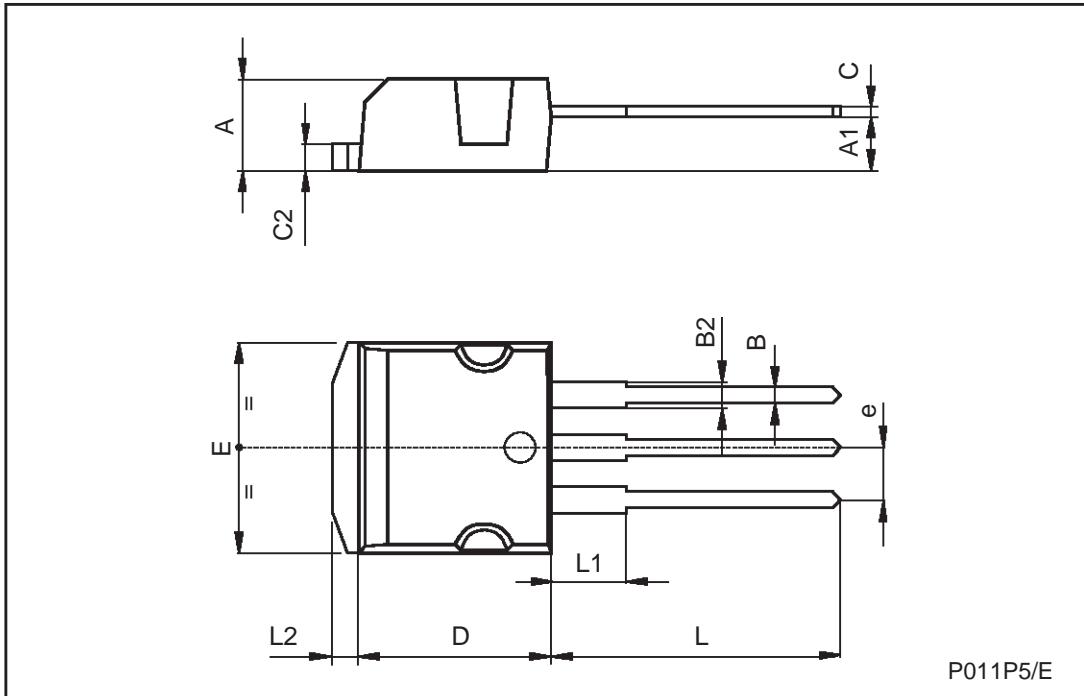
**D<sup>2</sup>PAK MECHANICAL DATA**

DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.4		4.6	0.173		0.181
A1	2.49		2.69	0.098		0.106
A2	0.03		0.23	0.001		0.009
B	0.7		0.93	0.027		0.036
B2	1.14		1.7	0.044		0.067
C	0.45		0.6	0.017		0.023
C2	1.23		1.36	0.048		0.053
D	8.95		9.35	0.352		0.368
D1		8			0.315	
E	10		10.4	0.393		
E1		8.5			0.334	
G	4.88		5.28	0.192		0.208
L	15		15.85	0.590		0.625
L2	1.27		1.4	0.050		0.055
L3	1.4		1.75	0.055		0.068
M	2.4		3.2	0.094		0.126
R		0.4			0.015	
V2	0°		8°			



**TO-262 (I<sup>2</sup>PAK) MECHANICAL DATA**

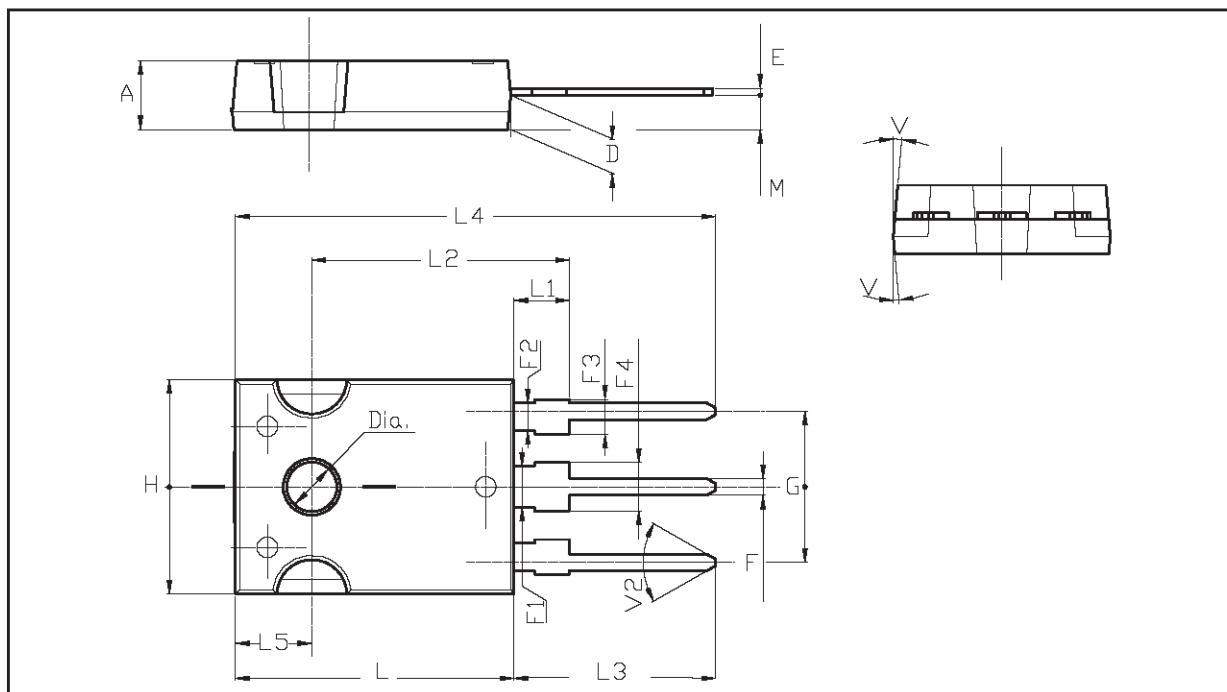
DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.4		4.6	0.173		0.181
A1	2.49		2.69	0.098		0.106
B	0.7		0.93	0.027		0.036
B2	1.14		1.7	0.044		0.067
C	0.45		0.6	0.017		0.023
C2	1.23		1.36	0.048		0.053
D	8.95		9.35	0.352		0.368
e	2.4		2.7	0.094		0.106
E	10		10.4	0.393		0.409
L	13.1		13.6	0.515		0.531
L1	3.48		3.78	0.137		0.149
L2	1.27		1.4	0.050		0.055



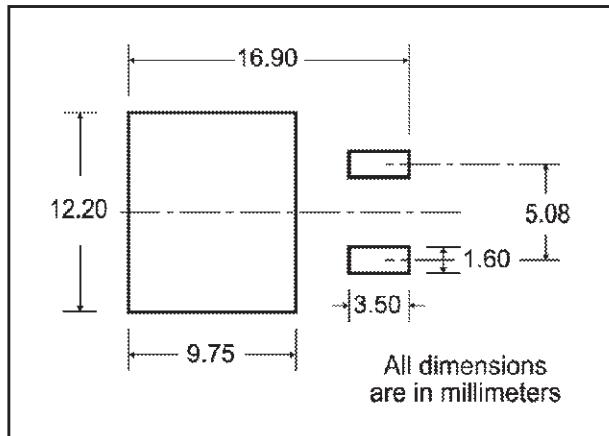
P011P5/E

## TO-247 MECHANICAL DATA

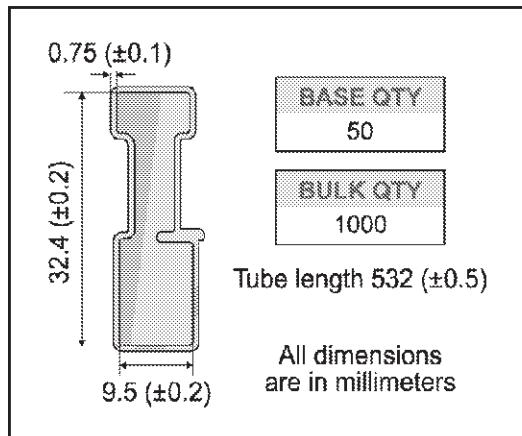
DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A	4.85		5.15	0.19		0.20
D	2.20		2.60	0.08		0.10
E	0.40		0.80	0.015		0.03
F	1		1.40	0.04		0.05
F1		3			0.11	
F2		2			0.07	
F3	2		2.40	0.07		0.09
F4	3		3.40	0.11		0.13
G		10.90			0.43	
H	15.45		15.75	0.60		0.62
L	19.85		20.15	0.78		0.79
L1	3.70		4.30	0.14		0.17
L2		18.50			0.72	
L3	14.20		14.80	0.56		0.58
L4		34.60			1.36	
L5		5.50			0.21	
M	2		3	0.07		0.11
V		5°			5°	
V2		60°			60°	
Dia	3.55		3.65	0.14		0.143



### D<sup>2</sup>PAK FOOTPRINT



### TUBE SHIPMENT (no suffix)\*



### TAPE AND REEL SHIPMENT (suffix "T4")\*

40 mm min. Access hole at slot location

Full radius

Tape slot in core for tape start 2.5mm min. width

TAPE MECHANICAL DATA

DIM.	mm		inch	
	MIN.	MAX.	MIN.	MAX.
A0	10.5	10.7	0.413	0.421
B0	15.7	15.9	0.618	0.626
D	1.5	1.6	0.059	0.063
D1	1.59	1.61	0.062	0.063
E	1.65	1.85	0.065	0.073
F	11.4	11.6	0.449	0.456
K0	4.8	5.0	0.189	0.197
P0	3.9	4.1	0.153	0.161
P1	11.9	12.1	0.468	0.476
P2	1.9	2.1	0.075	0.082
R	50		1.574	
T	0.25	0.35	0.0098	0.0137
W	23.7	24.3	0.933	0.956

REEL MECHANICAL DATA

DIM.	mm		inch	
	MIN.	MAX.	MIN.	MAX.
A			330	12.992
B	1.5		0.059	
C	12.8	13.2	0.504	0.520
D	20.2		0.795	
G	24.4	26.4	0.960	1.039
N	100		3.937	
T			30.4	1.197

BASE QTY	BULK QTY
1000	1000

10 pitches cumulative tolerance on tape  $+/- 0.2$  mm

Center line of cavity

User Direction of Feed

TRL

FEED DIRECTION

Bending radius R min.

## **STP13NK60Z, STP13NK60ZFP, STB13NK60Z, STB13NK60Z-1, STW13NK60Z**

---

Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specification mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of STMicroelectronics.

The ST logo is a trademark of STMicroelectronics

© 2000 STMicroelectronics – Printed in Italy – All Rights Reserved  
STMicroelectronics GROUP OF COMPANIES

Australia - Brazil - China - Finland - France - Germany - Hong Kong - India - Italy - Japan - Malaysia - Malta - Morocco -  
Singapore - Spain - Sweden - Switzerland - United Kingdom - U.S.A.

<http://www.st.com>