Features

- Temperature and Voltage Compensated Frequency
- Warning Indication of Lamp Failure by Means of Frequency Doubling
- Minimum Lamp Load for Flasher Operation ≥ 10 W
- Relay Output with High Current Carrying Capacity and Low Saturation Voltage
- Low Susceptibility to EMI



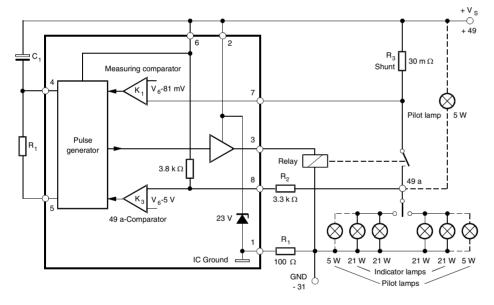
Flasher, 30 m Ω Shunt, Pilot Lamp to GND or $V_{\rm Batt}$

U2043B

Description

The integrated circuit U2043B is used in relay-controlled automotive flashers where a high EMC level is required. A lamp outage is indicated by frequency doubling during hazard mode as well as direction mode. The pilot lamp can be connected either to V_{Batt} or GND.

Figure 1. Block Diagram



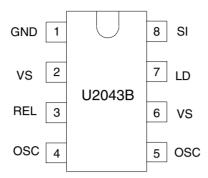


Rev. 4720A-AUTO-05/03



Pin Configuration

Figure 2. Pinning DIP8/SO8



Pin Description

Pin	Symbol	Function
1	GND	IC ground
2	VS	Supply voltage V _S
3	REL	Relay driver
4	OSC	C ₁ Oscillator
5	OSC	R ₁ Oscillator
6	VS	Supply voltage V _S
7	LD	Lamp failure detection
8	SI	Start input (49a)

Functional Description

GND (Pin 1) The integrated circuit is protected against damage via resistor R₄ to ground (-31) in the

case of battery reversal. An integrated protection circuit together with external resis-

tances R_2 and R_4 limits the current pulses in the IC.

Supply Voltage, The arrangement of the supply connections to pin 2 must be so as to ensure that, on the connection printed circuit board (PCB), the resistance of $V_{\rm S}$ to pin 6 is lower than that to V_s Power (Pin 2)

pin 2.

Relay Control Output The relay control output is a high-side driver with a low saturation voltage and capable of (Driver, Pin 3)

driving a typical automotive relay with a minimum coil resistance of 60 Ω .

Oscillator (Pin 4 and 5)

Flashing frequency, f₁, is determined by the R₁C₁ components as follows (see Figure 1):

$$f_1 = \frac{1}{R_1 \times C_1 \times 1.5} Hz$$

where

$$C_1 \le 47 \mu F$$

$$C_1 \le 47 \ \mu F$$

 $R_1 = 6.8 \ k\Omega \ to \ 510 \ k\Omega$

In the case of a lamp outage (see pin 7) the oscillator frequency is switched to the lamp outage frequency f_2 with $f_2 \approx 2.2 \times f_1$.

Duty cycle in normal flashing mode: 50%

Duty cycle in lamp outage mode: 40% (bright phase)

Supply Voltage, Sense (Pin 6)

For accurate monitoring via the shunt resistor, a minimized layer resistance from point V_S/shunt to pin 6 is recommended.

Lamp Outage Detection (Pin 7)

The lamp current is monitored via an external shunt resistor R₃ and an internal comparator K1 with its reference voltage of typically 81 mV (V_S = 12 V). The outage of one lamp out of two lamps is detected according to the following calculation:

Nominal current of 1 lamp:

$$21 \text{ W} / (\text{VS} = 12 \text{ V})$$
:

$$I_{lamp} = 1.75 A$$

Nominal current of 2 lamps:

$$2 \times 21 \text{ W/(V}_{S} = 12 \text{ V)}$$
:

$$I_{lamp} = 3.5 A$$

We recommend setting the detection threshold in the middle of the current range:

$$I_{outage} \approx 2.7 A$$

Thus, the shunt resistor is calculated as:

$$R_3 = V_T (K1)/I_{outage}$$

$$R_3 = 81 \text{ mV}/2.7 \text{ A} = 30 \text{ m}\Omega$$

Comparator K1's reference voltage is matched to the characteristics of filament lamps (see section "Control Signal Threshold").

The combination of the shunt resistor and the resistance of the wire harness prevents pin 7 from a too high voltage in the case of shorted lamps.

Start Input (Pin 8)

Start condition for flashing:

The voltage at pin 8 has to be less than V_S - 5 V (flasher switch closed).

Humidity and dirt may decrease the resistance between 49a and GND. If this leakage resistance is $> 5 \text{ k}\Omega$ the IC is still kept in its off-condition. In this case the voltage at pin 8 is greater than V_S - 5 V.

During the bright phase the voltage at pin 8 is above the K2 threshold, during the dark phase it is below the K3 threshold. For proper start conditions a minimum lamp wattage of 10 W is required.





Absolute Maximum Ratings

Reference point: pin 1

Parameters	Symbol	Value	Unit
Supply voltage, pins 2 and pin 6	V _S	16.5	V
Surge forward current			
$t_{\rm p} = 0.1 {\rm ms}$	I _{FSM}	1.5	Α
$t_p = 2 \text{ ms}$	I _{FSM}	1.0	Α
$t_p = 2 \text{ ms}$	I _{FSM}	50	mA
Output current, pin 3	Io	0.3	A
Power Dissipation	· · · · · · · · · · · · · · · · · · ·		*
T _{amb} = 95°C, DIP8	P _{tot}	420	mW
SO8	P _{tot}	340	mW
T _{amb} = 60°C, DIP8	P _{tot}	690	mW
SO8	P _{tot}	560	mW
Junction temperature	T _j	150	°C
Ambient temperature range	T _{amb}	-40 to +95	°C
Storage temperature range	T _{stg}	-55 to +150	°C

Thermal Resistance

Parameters	Symbol	Value	Unit
Junction ambient, DIP8	R_{thJA}	110	K/W
Junction ambient, SO8	R _{thJA}	160	K/W

Electrical Characteristics

Typical values under normal operation in application circuit Figure 1, V_S (+49, pin 2 and pin 6) = 12 V. Reference point ground (-31), T_{amb} = 25°C, unless otherwise specified

Parameters	Test Conditions	Symbol	Min.	Тур.	Max.	Unit
Supply voltage range	Pins 2, 6	V _S (+49)		9 to 15		V
Supply current	Dark phase or stand-by, pins 2, 6	Is		4.5	8	mA
Supply current	Bright phase, pins 2, 6	Is		7.0	11	mA
Relay output	Saturation voltage $I_O = 150 \text{ mA},$ $V_S = 9 \text{ V (pin 3)}$	V _O			1.0	V
Relay output reverse current	Pin 3	Io			0.1	mA
Relay coil resistance		R_L	60			Ω
Start delay	First bright phase	t _{on}			10	ms
Frequency determining resistor		R ₁	6.8		510	kΩ
Frequency determining capacitor		C ₁			47	μF
Frequency tolerance	Normal flashing, basic frequency f ₁ not including the tolerance of the external components R ₁ and C ₁	Δf_1	-5		+5	%
Driedat a cric d	Basic frequency f ₁	Δf_1	47		53	%
Bright period	Control frequency f ₂	Δf_2	37		45	%
Frequency increase	Lamp outage	f ₂	2.15 × f ₁		2.3 × f ₁	Hz

Electrical Characteristics (Continued)

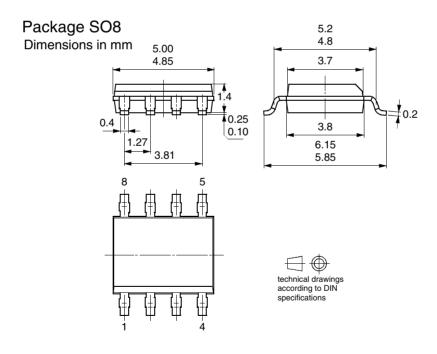
Typical values under normal operation in application circuit Figure 1, V_S (+49, pin 2 and pin 6) = 12 V. Reference point ground (-31), T_{amb} = 25°C, unless otherwise specified

Parameters	Test Conditions	Symbol	Min.	Тур.	Max.	Unit
	V _S = 15 V (pin 7)	V_{R3}	85	91	97	mV
Control signal threshold	$V_S = 9 V$	V_{R3}	66	71	76	mV
	V _S = 12 V	V_{R3}	76	81	87	mV
Leakage resistance	49a to GND	R_p		2	5	kΩ
Lamp load		P _L	10			W

Ordering Information

Extended Type Number	Package	Remarks
U2043B	DIP8	_
U2043B-FP	SO9	-

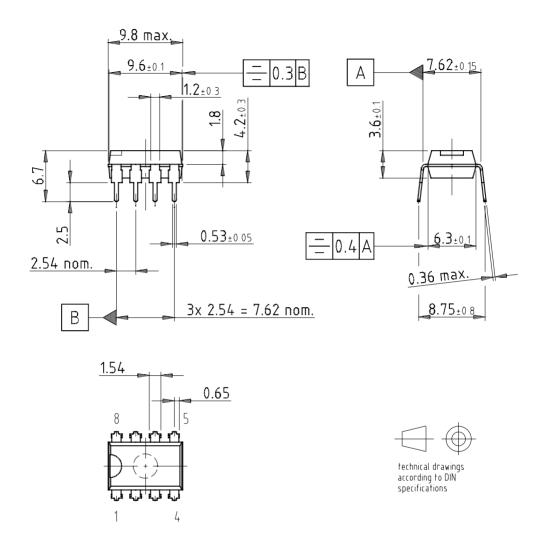
Package Information







Package: DIP 8
Dimensions in mm



Drawing-No.: 6.543-5040.01-4

Issue: 1; 16.01.02



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